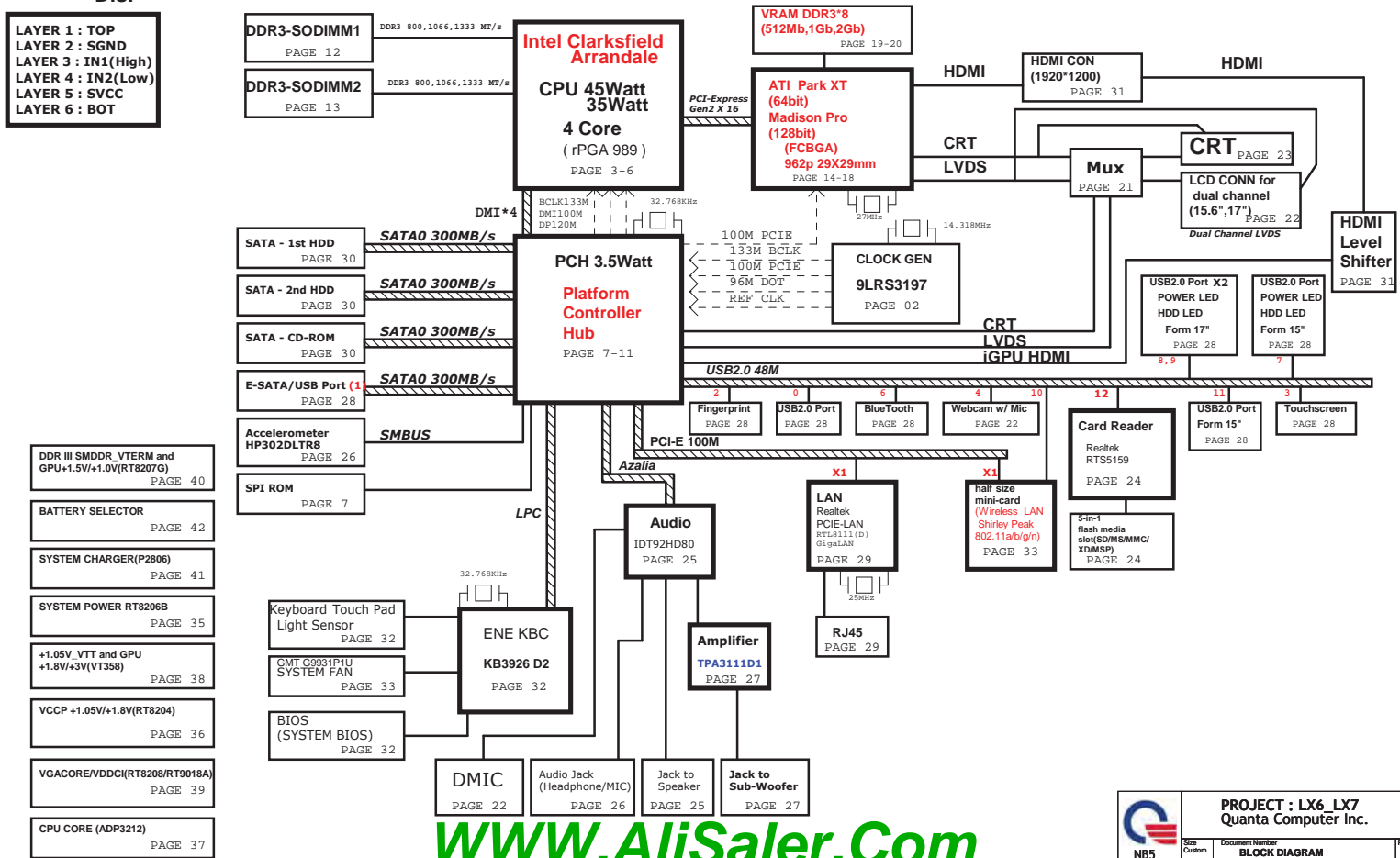
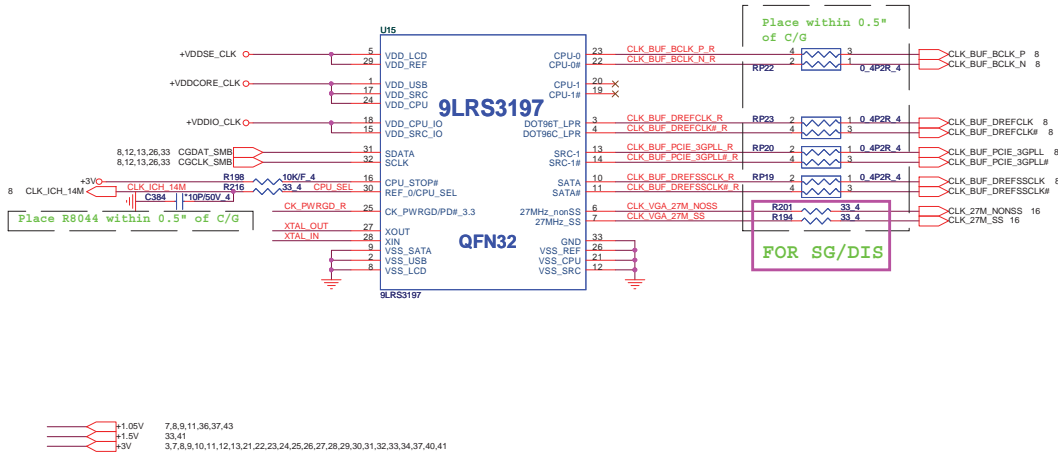
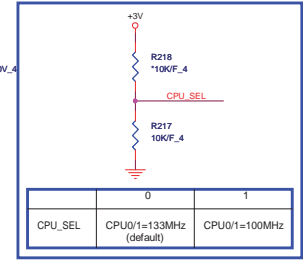
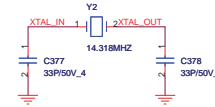
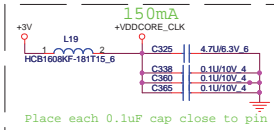
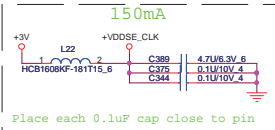
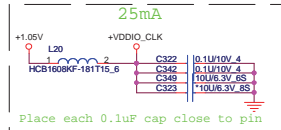


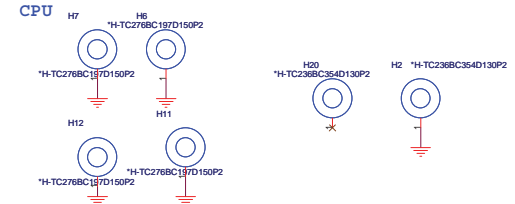
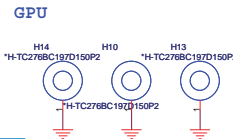
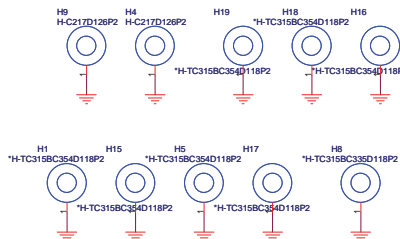
LX6/7 (Liverpool) BLOCK DIAGRAM

01

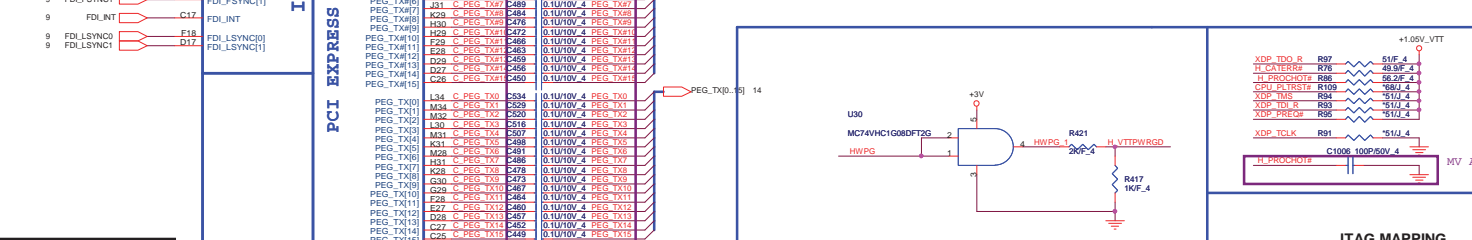
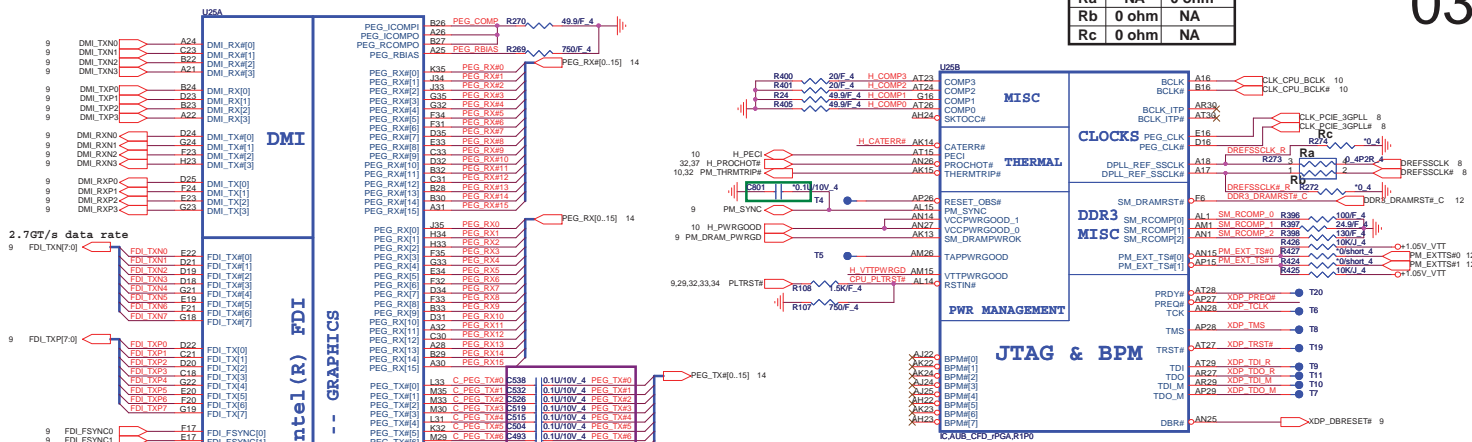




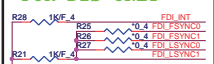
HOLE



| | | |
|----|-------|--------|
| | DIS | SG/UMA |
| Ra | NA | 0 ohm |
| Rb | 0 ohm | NA |
| Rc | 0 ohm | NA |

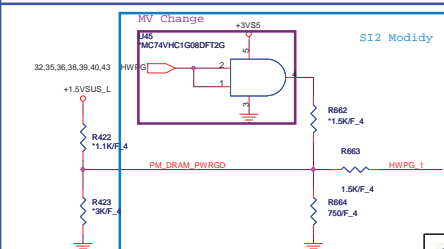


FOR DIS ONLY



FOR SG/DIS

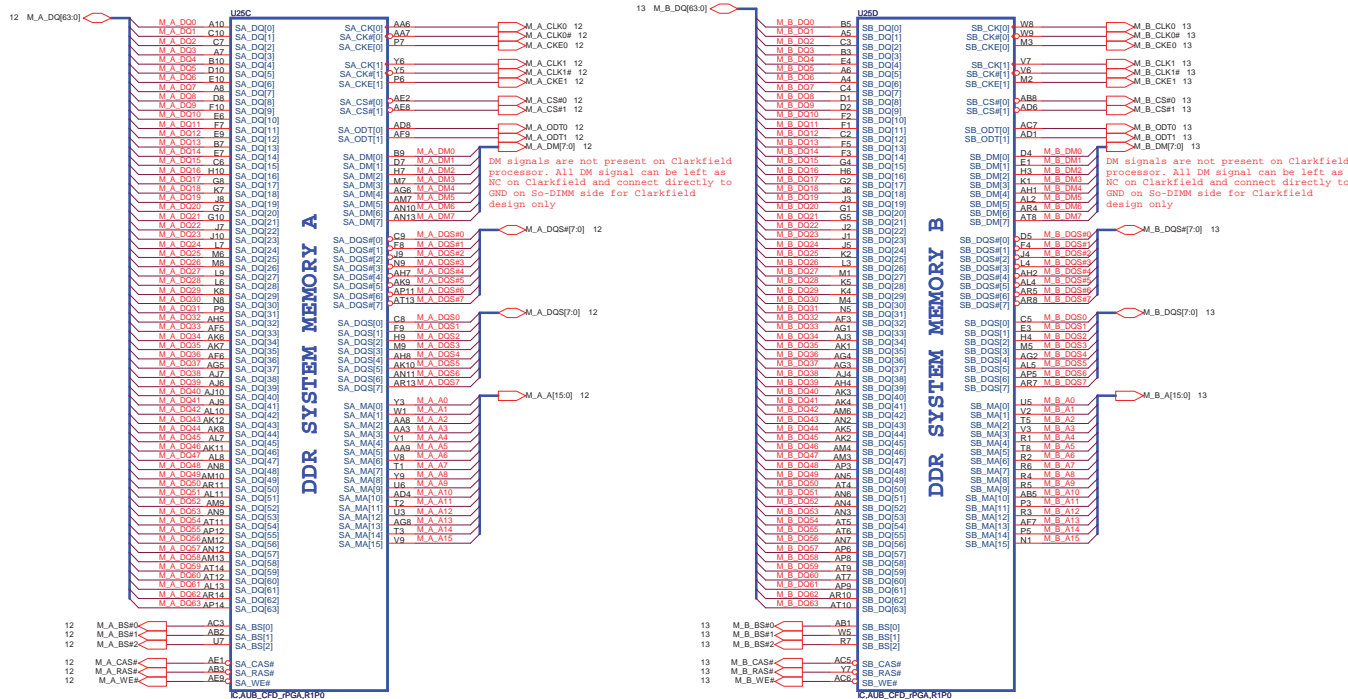
SI Del R416,R393,R407,R406,R395,Q31,Q30,Q32



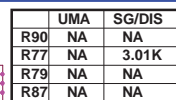
| | |
|----------------------|---|
| Scan Chain (Default) | STUFF -> Ra, Rc, Re NO STUFF -> Rb, Rd |
| CPU Only | STUFF -> Ra, Rb NO STUFF -> Rc, Rd, Re |
| GMCH Only | STUFF -> Rd, Re NO STUFF -> Ra, Rb, Rc |

PROJECT : LX6_LX7

AUBURNDALE/CLARKSFIELD PROCESSOR (DDR3)







| | | | |
|--|----------------------------------|---|---------------|
|  NB5 | Size Custom | Document Number PROCESSER 4/4 (GND) | Rev 1/ |
| | Date: Tuesday, February 02, 2010 | | Sheet 6 of 44 |

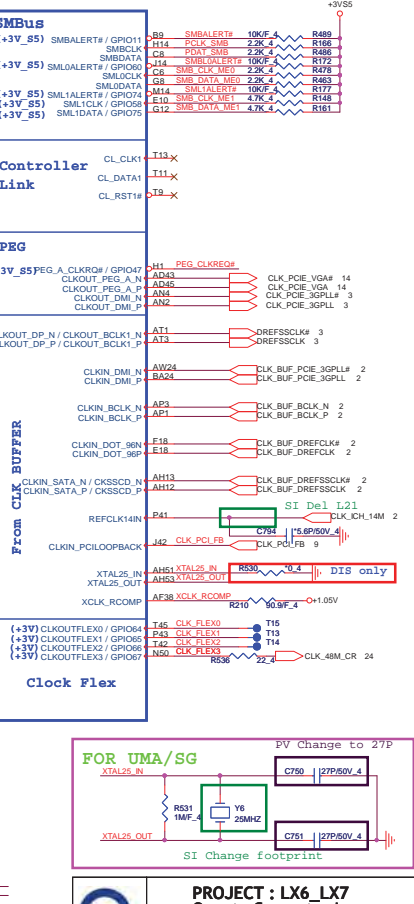
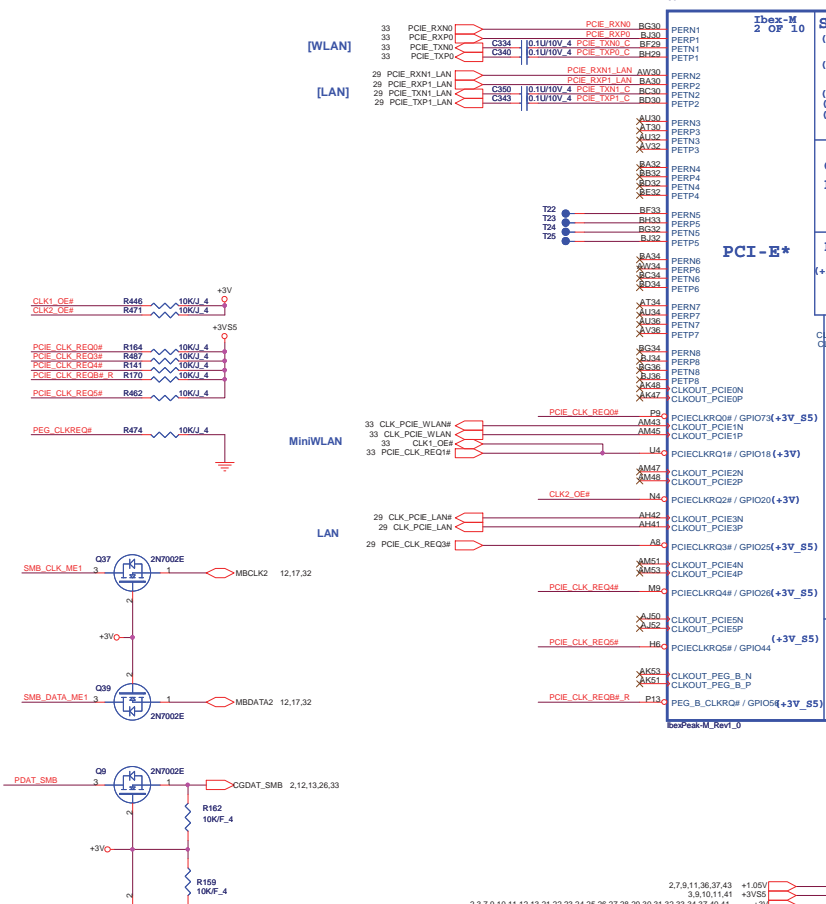
| | 1 | 0 |
|---|--|--|
| CFG4 (Display Port Presence) | Disabled; No Physical Display Port attached to Embedded Display Port | Enabled; An external Display port device is connected to the Embedded Display port |
| CFG0 (PCI-E Press Configuration Select) | Single PEG | Bifurcation enabled |
| CFG1 (PCI-E Press Status Latch (Reversed)) | Link numbers 0 to 3 | Link numbers Reversed 3 to 0 |

The Clarkfield processor's PCI Express interface may not meet PCI Express 2.0 jitter specifications. Intel recommends placing a 3.0k \pm 5% pull down resistor to VSS on CFG[7] pin for both rPGA and BGA components. This pull down resistor should be removed when this issue is fixed.

| | | |
|---|--|-----------|
| PROJECT : LX6_LX7 Quanta Computer Inc. | | |
| Size Custom | Document Number PCH 1/5 (SATA,HDA,LPC) | Rev 1A |

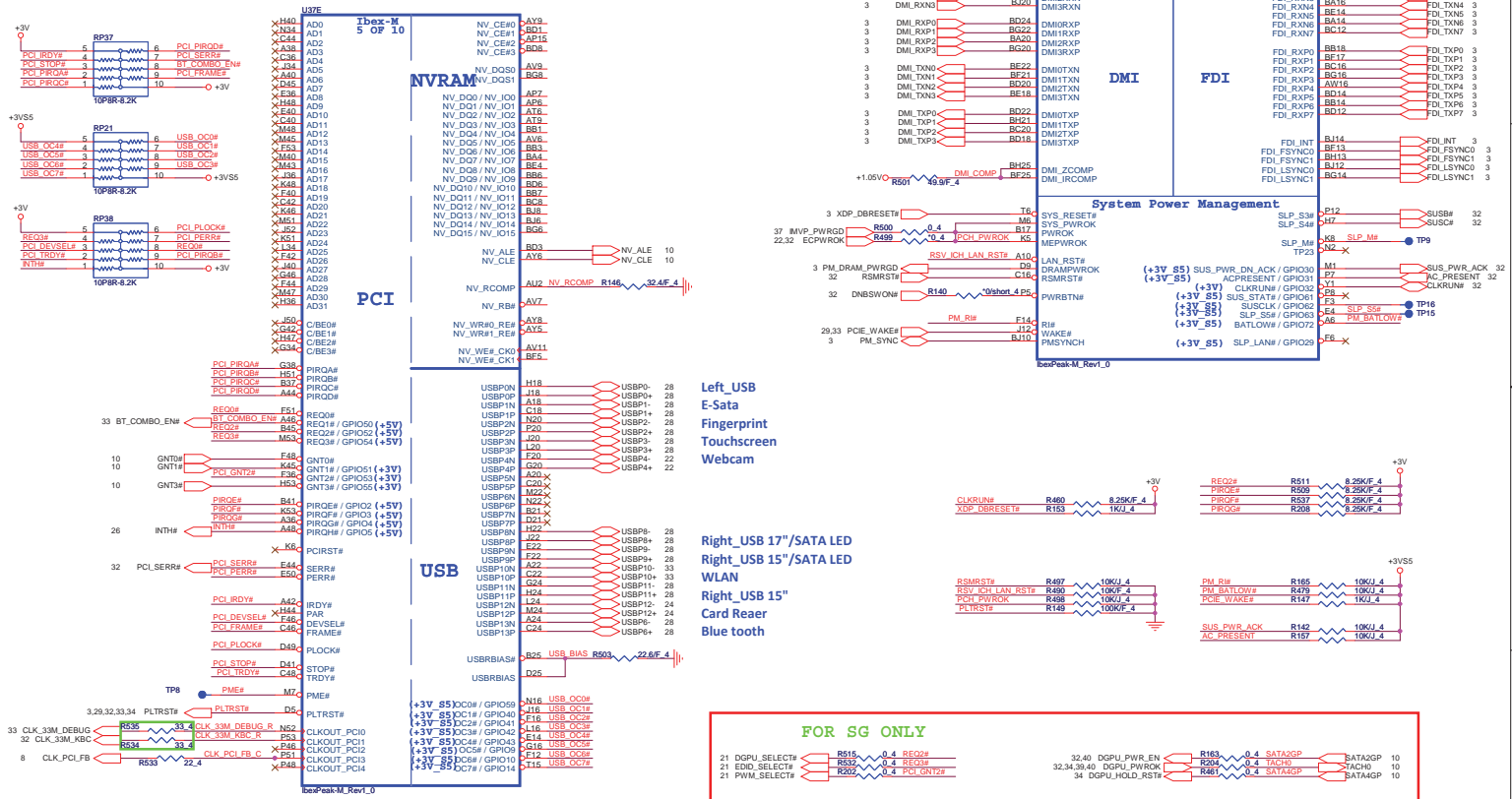
IBEX PEAK-M (GND)

| | | | |
|------|--------|--------|------|
| AY7 | VSS159 | VSS259 | H49 |
| B11 | VSS160 | VSS260 | H5 |
| B15 | VSS161 | VSS261 | J4 |
| B19 | VSS162 | VSS262 | K11 |
| B23 | VSS163 | VSS263 | K43 |
| B31 | VSS164 | VSS264 | M47 |
| B35 | VSS165 | VSS265 | K7 |
| B39 | VSS166 | VSS266 | L18 |
| B43 | VSS167 | VSS267 | L19 |
| B47 | VSS168 | VSS268 | L22 |
| B51 | VSS169 | VSS269 | L23 |
| B55 | VSS170 | VSS270 | L36 |
| B59 | VSS171 | VSS271 | L37 |
| B63 | VSS172 | VSS272 | M12 |
| B67 | VSS173 | VSS273 | M12 |
| B71 | VSS174 | VSS274 | M20 |
| B75 | VSS175 | VSS275 | M20 |
| B79 | VSS176 | VSS276 | M38 |
| B83 | VSS177 | VSS277 | M38 |
| B87 | VSS178 | VSS278 | M38 |
| B91 | VSS179 | VSS279 | M38 |
| B95 | VSS180 | VSS280 | M46 |
| BC10 | VSS181 | VSS281 | M46 |
| BC14 | VSS182 | VSS282 | M46 |
| BC18 | VSS183 | VSS283 | M46 |
| BC22 | VSS184 | VSS284 | M8 |
| BC26 | VSS185 | VSS285 | P11 |
| BC30 | VSS186 | VSS286 | P11 |
| BC34 | VSS187 | VSS287 | P22 |
| BC38 | VSS188 | VSS288 | P22 |
| BC42 | VSS189 | VSS289 | P22 |
| BC46 | VSS190 | VSS290 | P30 |
| BC50 | VSS191 | VSS291 | P30 |
| BC54 | VSS192 | VSS292 | P45 |
| BC58 | VSS193 | VSS293 | P45 |
| BC62 | VSS194 | VSS294 | P45 |
| BC66 | VSS195 | VSS295 | P45 |
| BC70 | VSS196 | VSS296 | R22 |
| BC74 | VSS197 | VSS297 | R22 |
| BC78 | VSS198 | VSS298 | T41 |
| BC82 | VSS199 | VSS299 | T49 |
| BC86 | VSS200 | VSS300 | T49 |
| BC90 | VSS201 | VSS301 | T8 |
| BC94 | VSS202 | VSS302 | T8 |
| BC98 | VSS203 | VSS303 | U31 |
| BC02 | VSS204 | VSS304 | U31 |
| BC06 | VSS205 | VSS305 | U32 |
| BC10 | VSS206 | VSS306 | U32 |
| BC14 | VSS207 | VSS307 | P38 |
| BC18 | VSS208 | VSS308 | P38 |
| BC22 | VSS209 | VSS309 | P11 |
| BC26 | VSS210 | VSS310 | V13 |
| BC30 | VSS211 | VSS311 | V20 |
| BC34 | VSS212 | VSS312 | V22 |
| BC38 | VSS213 | VSS313 | V31 |
| BC42 | VSS214 | VSS314 | V31 |
| BC46 | VSS215 | VSS315 | V31 |
| BC50 | VSS216 | VSS316 | V32 |
| BC54 | VSS217 | VSS317 | V38 |
| BC58 | VSS218 | VSS318 | V38 |
| BC62 | VSS219 | VSS319 | V46 |
| BC66 | VSS220 | VSS320 | V46 |
| BC70 | VSS221 | VSS321 | V46 |
| BC74 | VSS222 | VSS322 | V46 |
| BC78 | VSS223 | VSS323 | V46 |
| BC82 | VSS224 | VSS324 | V46 |
| BC86 | VSS225 | VSS325 | V46 |
| BC90 | VSS226 | VSS326 | V46 |
| BC94 | VSS227 | VSS327 | V52 |
| BC98 | VSS228 | VSS328 | V52 |
| BC02 | VSS229 | VSS329 | V52 |
| BC06 | VSS230 | VSS330 | V11 |
| BC10 | VSS231 | VSS331 | V11 |
| BC14 | VSS232 | VSS332 | V11 |
| BC18 | VSS233 | VSS333 | V28 |
| BC22 | VSS234 | VSS334 | V28 |
| BC26 | VSS235 | VSS335 | V30 |
| BC30 | VSS236 | VSS336 | V30 |
| BC34 | VSS237 | VSS337 | V32 |
| BC38 | VSS238 | VSS338 | V32 |
| BC42 | VSS239 | VSS339 | V32 |
| BC46 | VSS240 | VSS340 | V38 |
| BC50 | VSS241 | VSS341 | V38 |
| BC54 | VSS242 | VSS342 | V38 |
| BC58 | VSS243 | VSS343 | V38 |
| BC62 | VSS244 | VSS344 | V38 |
| BC66 | VSS245 | VSS345 | V42 |
| BC70 | VSS246 | VSS346 | V42 |
| BC74 | VSS247 | VSS347 | AD51 |
| BC78 | VSS248 | VSS348 | AD47 |
| BC82 | VSS249 | VSS349 | AD47 |
| BC86 | VSS250 | VSS350 | AM5 |
| BC90 | VSS251 | VSS351 | AT12 |
| BC94 | VSS252 | VSS352 | AK6 |
| BC98 | VSS253 | VSS353 | AK6 |
| BC02 | VSS254 | VSS354 | AK6 |
| BC06 | VSS255 | VSS355 | AK39 |
| BC10 | VSS256 | VSS356 | AK39 |
| BC14 | VSS257 | VSS357 | AK14 |
| BC18 | VSS258 | VSS358 | AK14 |

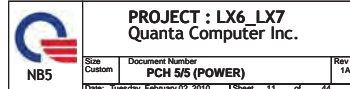
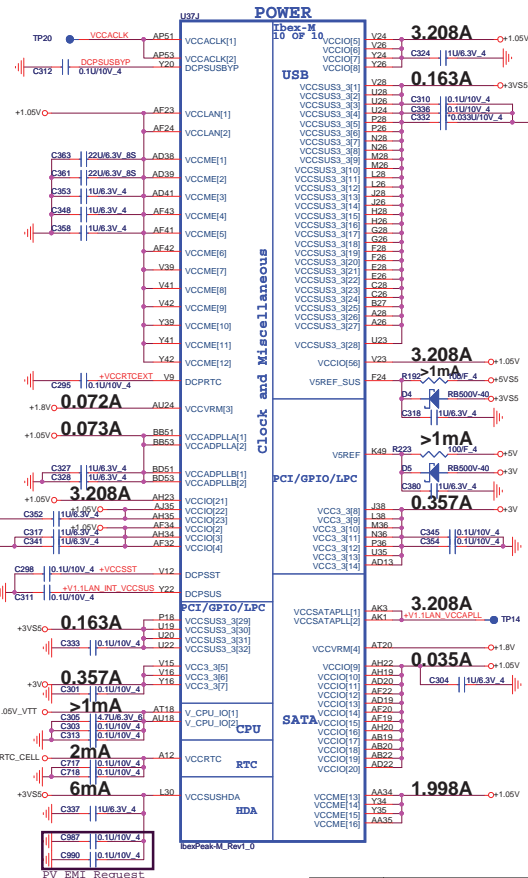
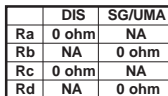


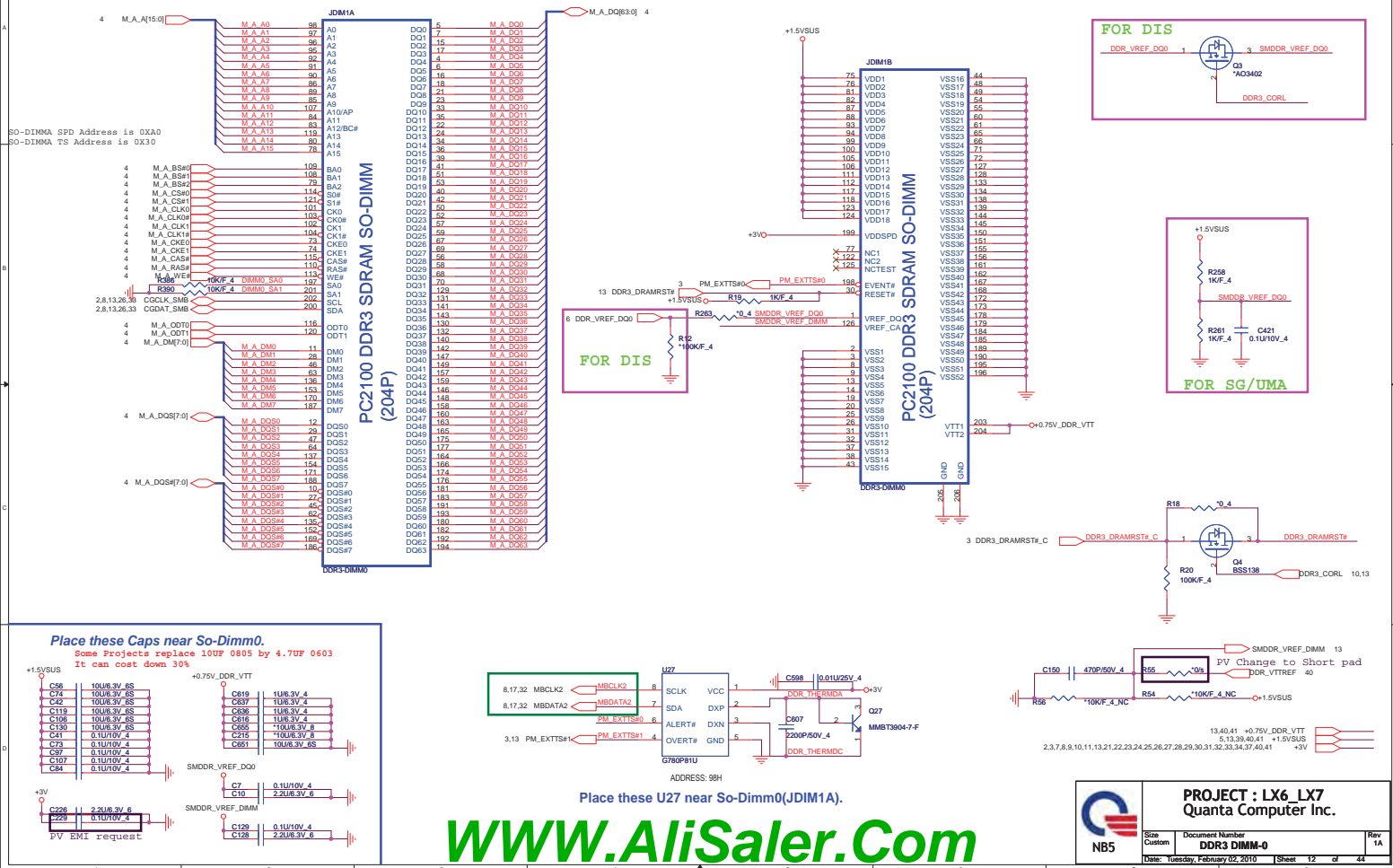
IBEX PEAK-M (DMI,FDI,GPIO)

IBEX PEAK-M (PCI,USB,NVRAM)

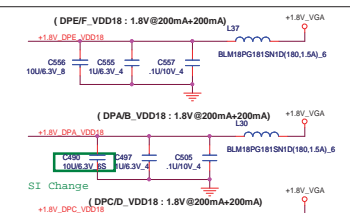
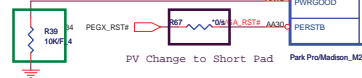


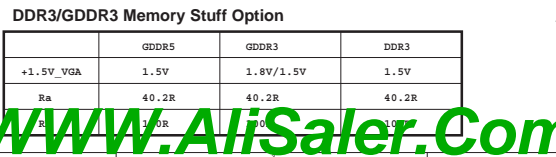


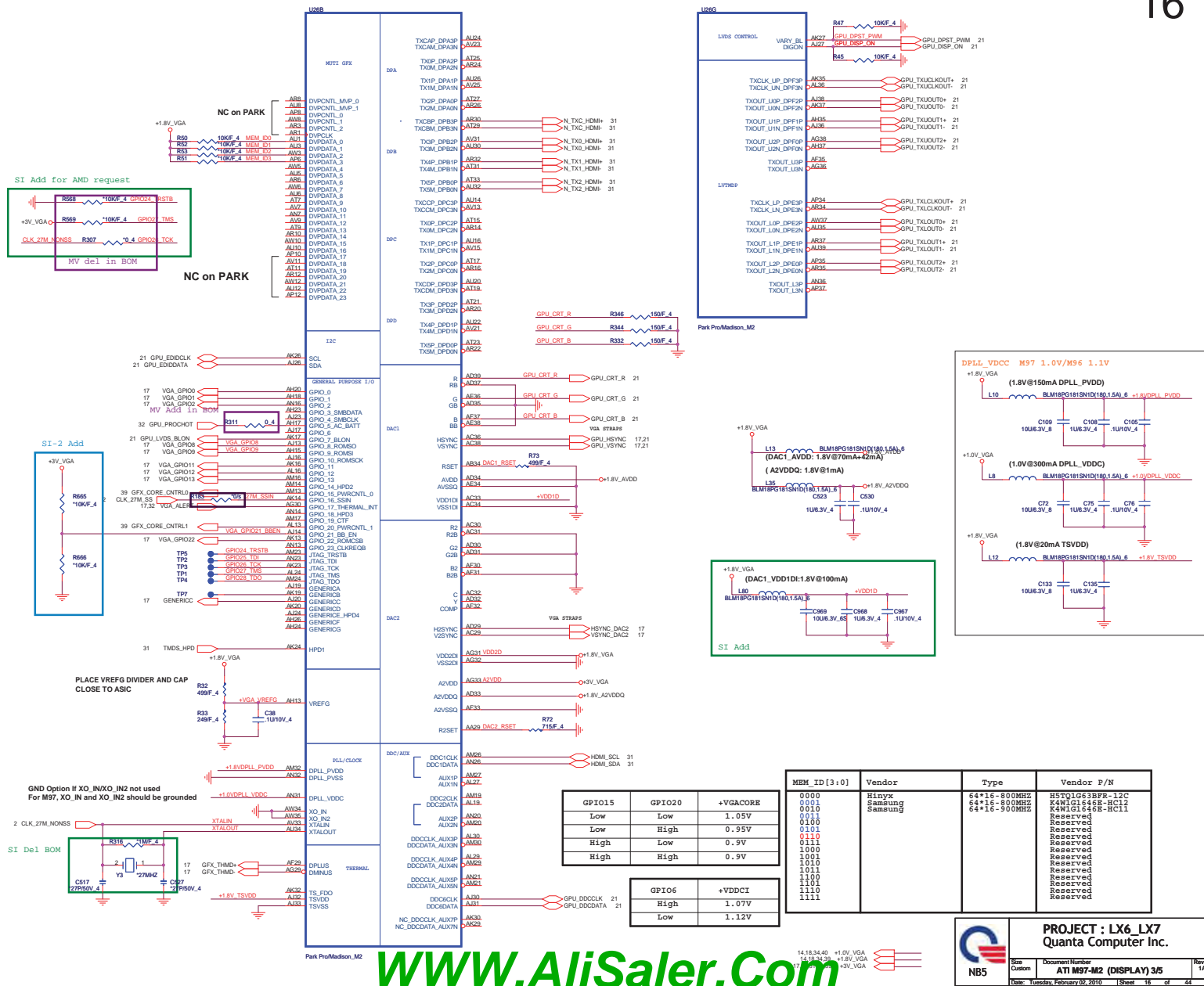


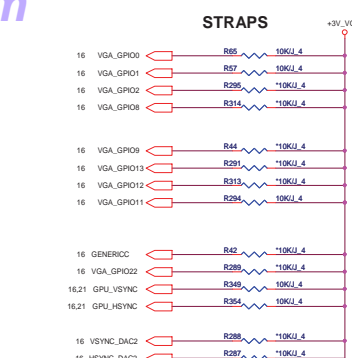
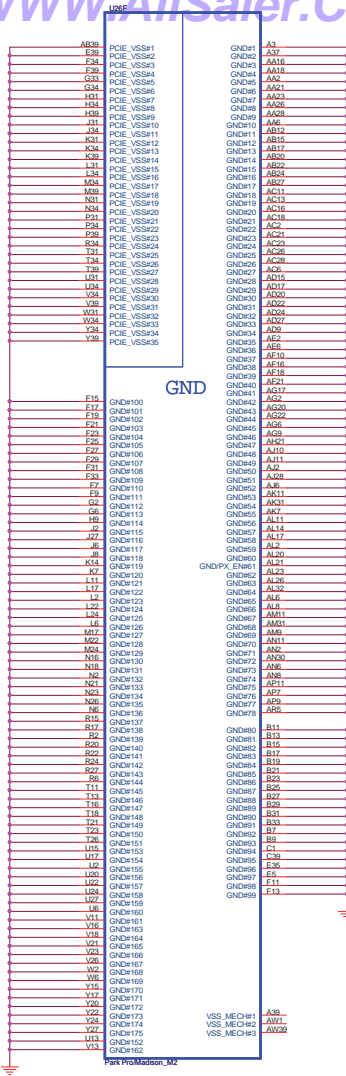












Memory Aperture size fix 256M

| GPIO9 BIOSROM | | GPIO13 ROMIDCFG2 | GPIO12 ROMIDCFG1 | GPIO11 ROMIDCFG0 |
|------------------|------|---------------------|---------------------|---------------------|
| 0 | 128M | 0 | 0 | 0 |
| 0 | 256M | 0 | 0 | 1 |
| 0 | 64M | 0 | 1 | 0 |
| 0 | 32M | 0 | 1 | 1 |
| 0 | 512M | 1 | 0 | 0 |
| 0 | 1G | 1 | 0 | 1 |
| 0 | 2G | 1 | 1 | 0 |
| 0 | 4G | 1 | 1 | 1 |

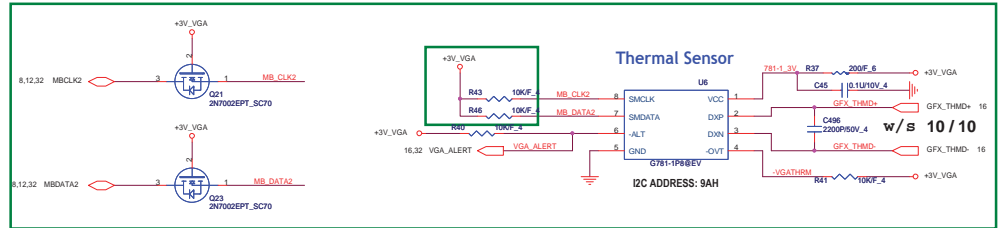
It is a shared pin strap with CONFIG[2:0] if BIOS_ROM_EN is set to 0.

| CONFIGURATION STRAPS | | | RECOMMENDED SETTINGS 0= DO NOT INSTALL RESISTOR # = INSTALL 10K RESISTOR X = DESIGN DEPENDANT NA = NOT APPLICABLE |
|----------------------------|--------------------------|--|---|
| STRAPS | PIN | DESCRIPTION OF DEFAULT SETTINGS | |
| TX_PWRS_ENB | GPIO0 | Transmitter Power Savings Enable 0: 50% Tx output swing for mobile mode 1: 100% Tx output swing (Default setting for Desktop) | 1 |
| TX_DEEMPH_EN | GPIO1 | PCI Express Transmitter De-emphasis Enable 0: Tx de-emphasis disabled for mobile mode 1: Tx de-emphasis enabled (Default setting for Desktop) | 1 |
| BIF_GEN2_EN_A | GPIO2 | 0 = Advertises the PCI-E device as 2.5 GT/s capable at power-on. 1 = Advertises the PCI-E device as 5.0 GT/s capable at power-on. 1: 5 GT/s capability will be controlled by software. | 0 |
| RSVD BIF_VGA_DS RSVD | GPIO8 GPIO9 GPIO21 | VGA ENABLED | 0 0 0 |
| BIOS_ROM_EN | GPIO_22_ROMIDCS | ENABLE EXTERNAL BIOS ROM | 0 |
| ROMIDCFG2(0) | GPIO(13-11) | SERIAL ROM TYPE OR MEMORY APERTURE SIZE SELECT | 0 0 1 |
| VIP_DEVICE_STRAP_ENA | V2SYN | IGNORE VIP DEVICE STRAPS | 0 |
| RSVD AUD1 AUD0 | GENERIC HSYN V2SYN | AUD1(1) AUD0(0) 0: 0 No audio function 0: 1 Audio for DisplayPort and HDMI if dongle is detected 1: 0 Audio for DisplayPort only 1: 1 Audio for both DisplayPort and HDMI | 0 0 11 |

AMD RESERVED CONFIGURATION STRAPS

ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET

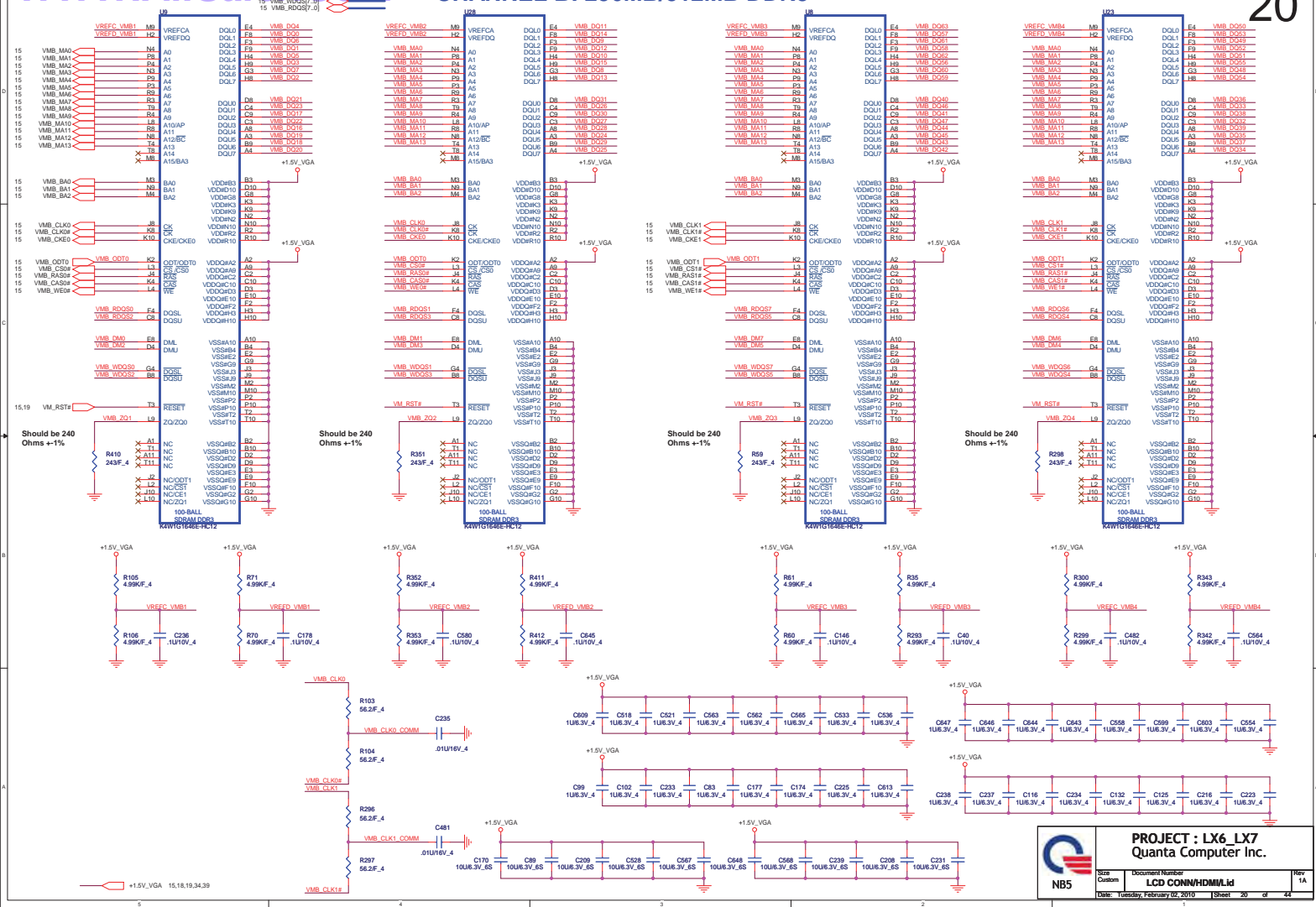
PULLUP PADS ARE NOT REQUIRED FOR THESE STRAPS BUT IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET

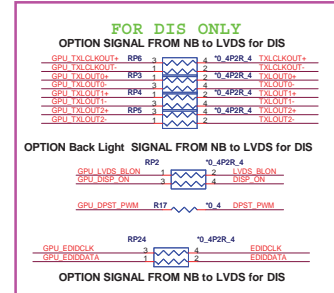
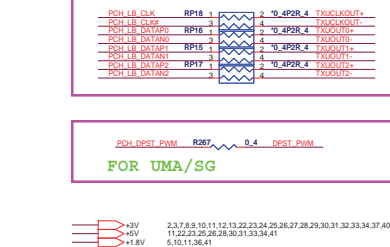
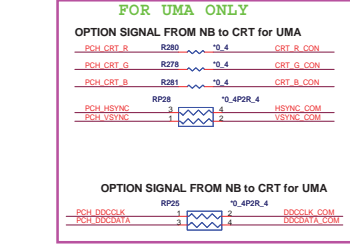
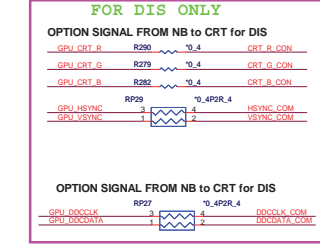
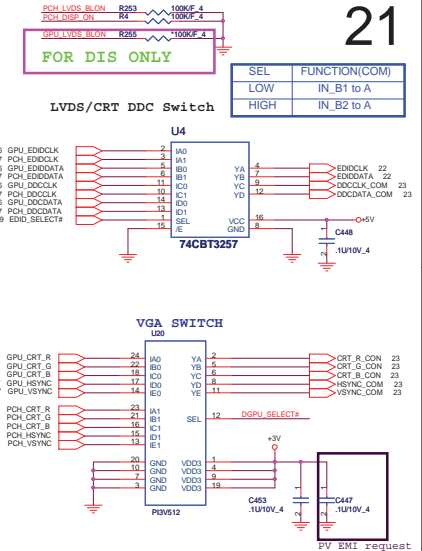
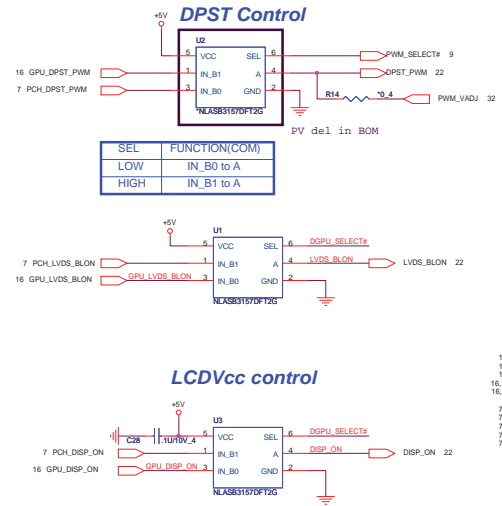
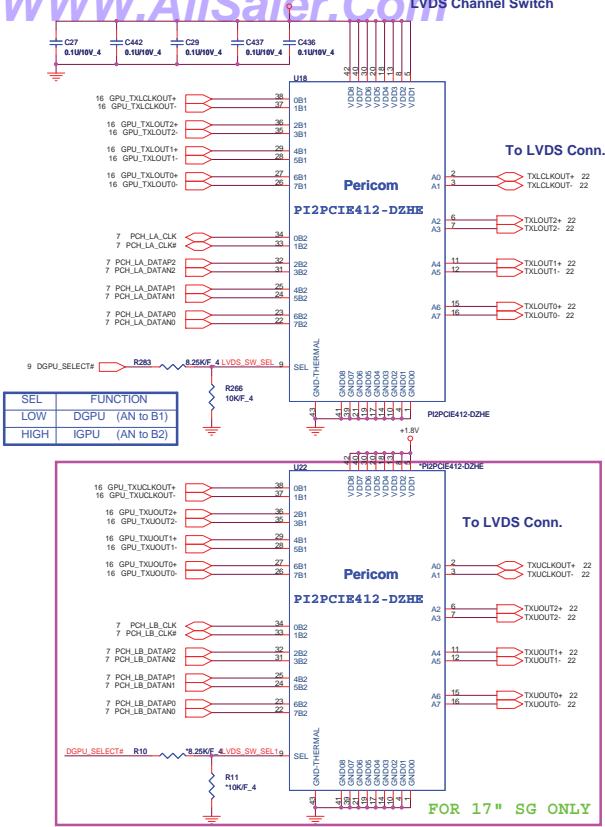


| PROJECT : LX6_LX7 Quanta Computer Inc. | | | |
|---|--|-----------|-------|
| NB5 | Document Number ATI M97(GND&StrapTher)4/5 | Rev 1A | |
| Date: Tuesday, February 02, 2010 | Sheet | 17 | of 44 |







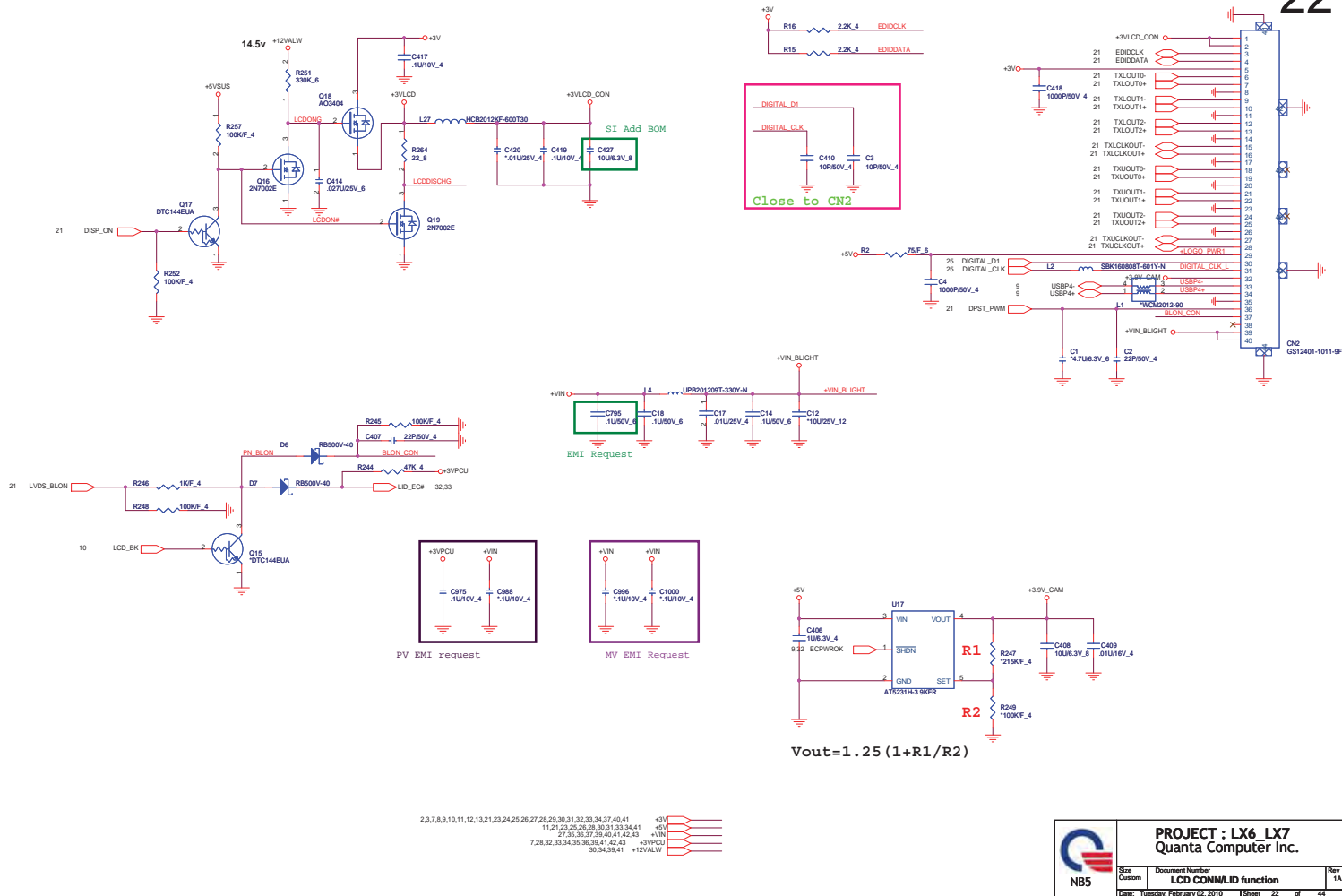


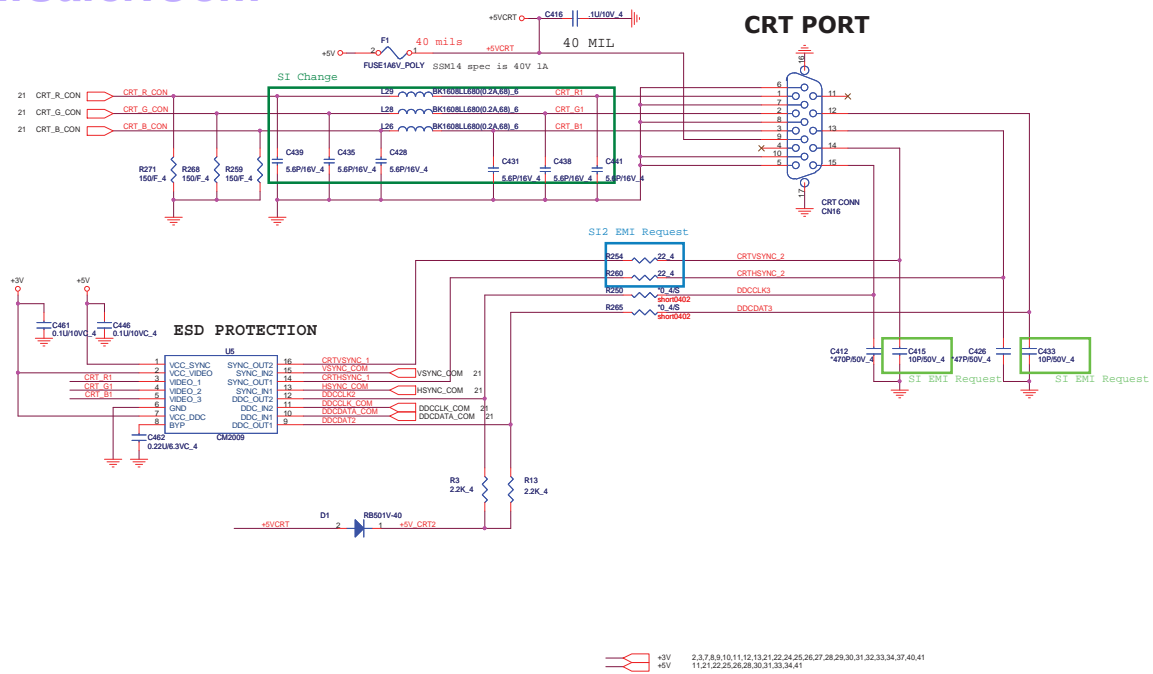
PROJECT : LX6_LX7
Quanta Computer Inc.


SWITCHABLE

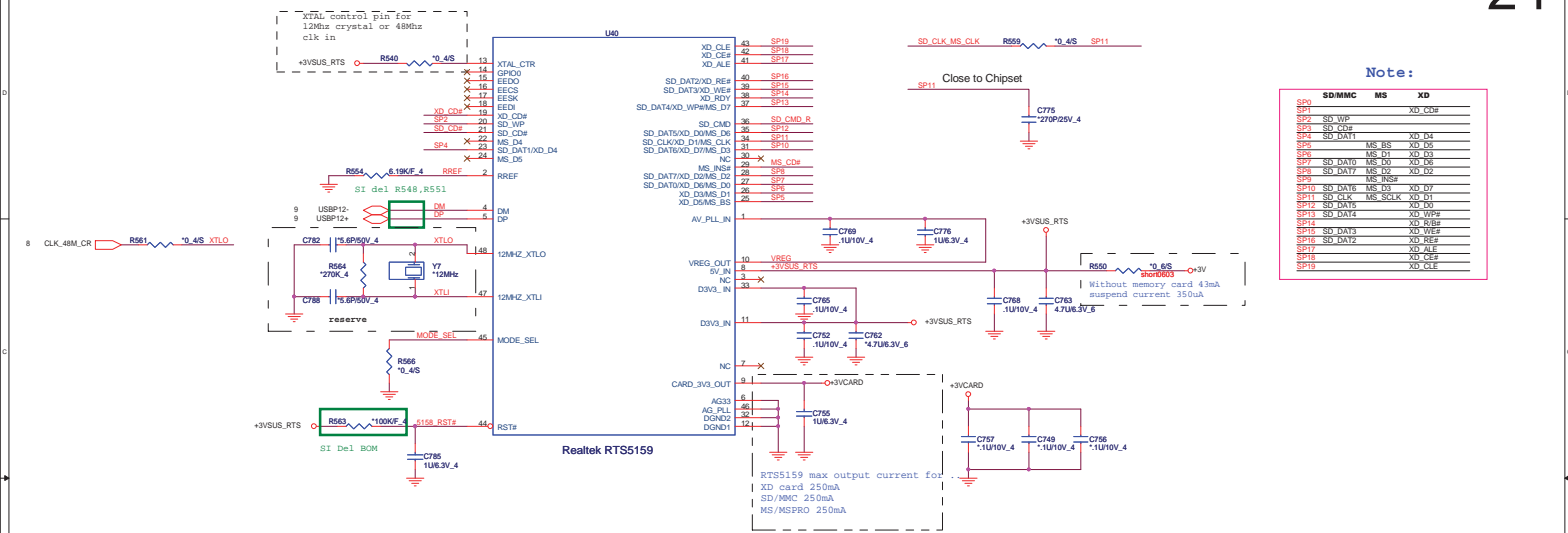
Rev 1A

Doc: Tuesday, February 02, 2010 | Sheet 21 of 44

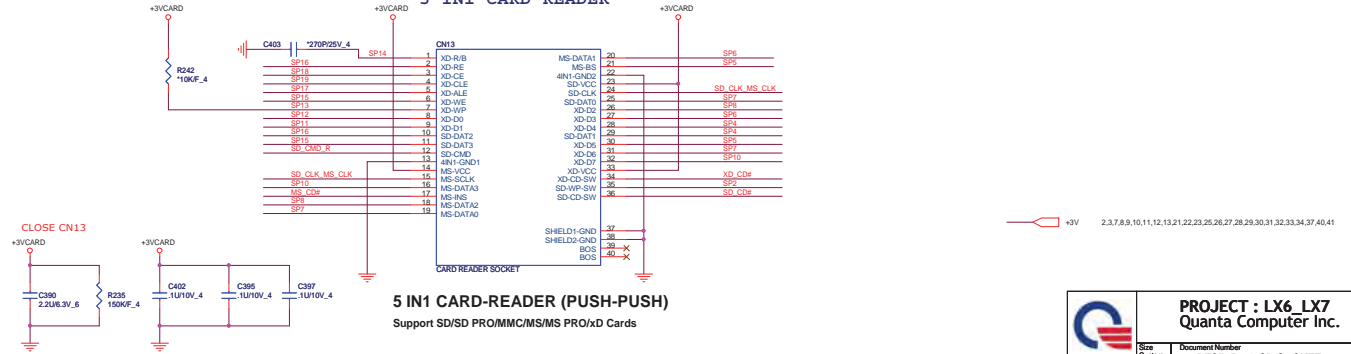




| | | |
|---|----------------------------------|-----------|
|  PROJECT : LX6_LX7 Quanta Computer Inc. | | |
| S/N Custom | Document Number CRT/HDMI Conn | Rev 1A |
| Date: Tuesday, February 03, 2010 | Sheet 23 of 44 | |



XD,MMC/SD,MS/MSP
5 IN1 CARD READER



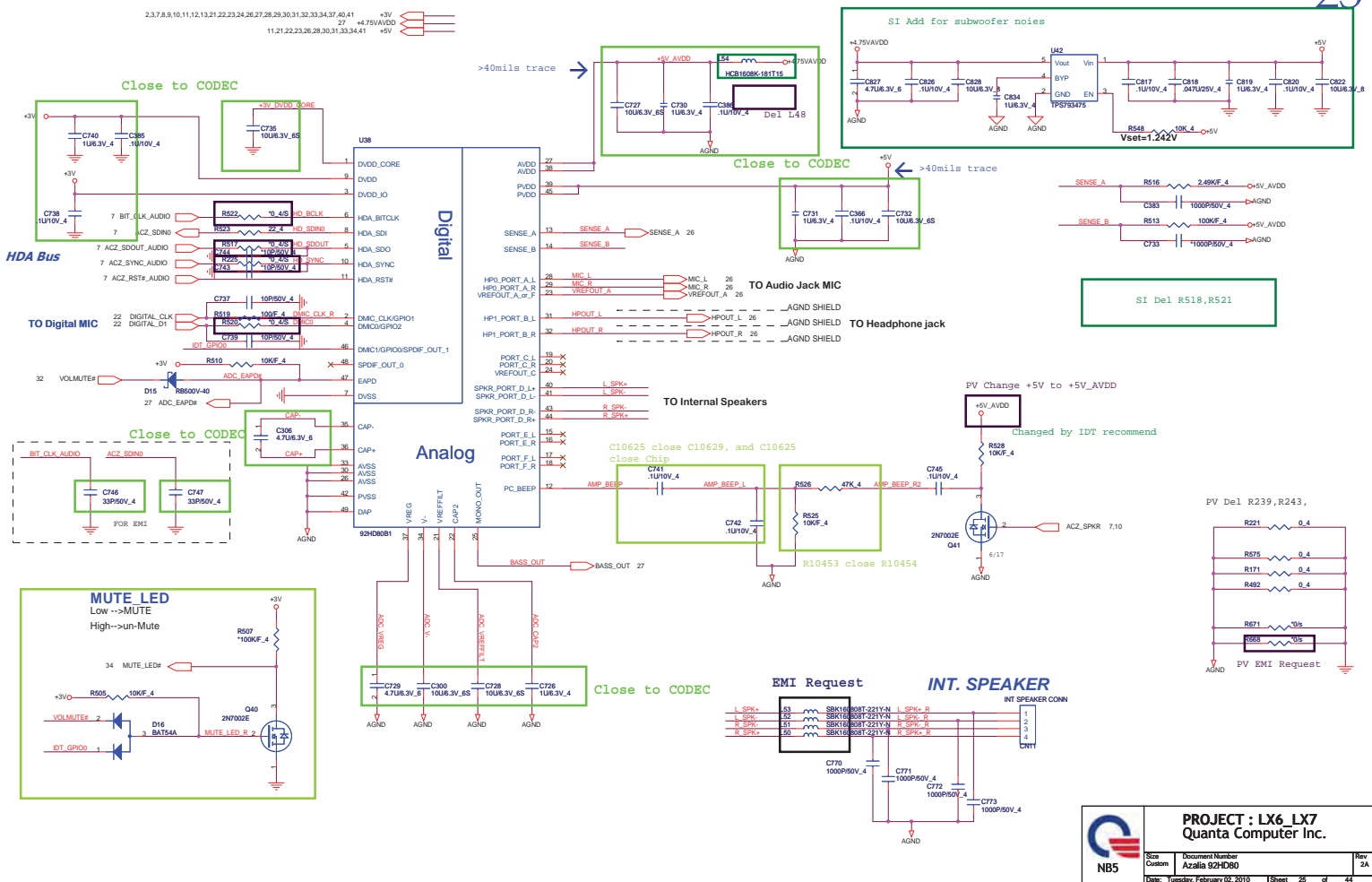
5 IN1 CARD-READER (PUSH-PUSH)
Support SD/SD PRO/MMC/MS/MS PRO/xD Cards

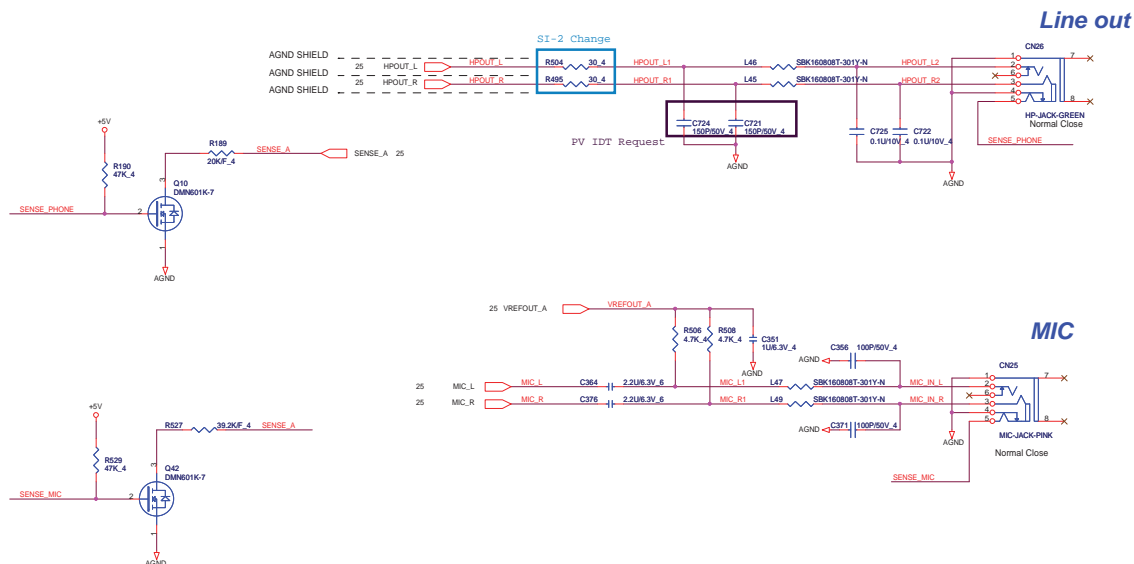
Support SD/SD PRO/MMC/MS/MS PRO/xD Cards



PROJECT : LX6_LX7
Quanta Computer Inc.

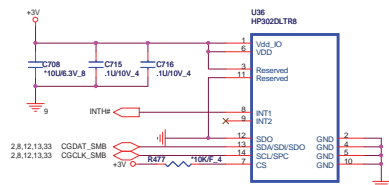
| | | |
|----------------------------------|---|-----------|
| Size Custom | Document Number RTS5159 & CR SOCKET | Rev 1A |
| Date: Tuesday, February 02, 2010 | Sheet 24 of 44 | |

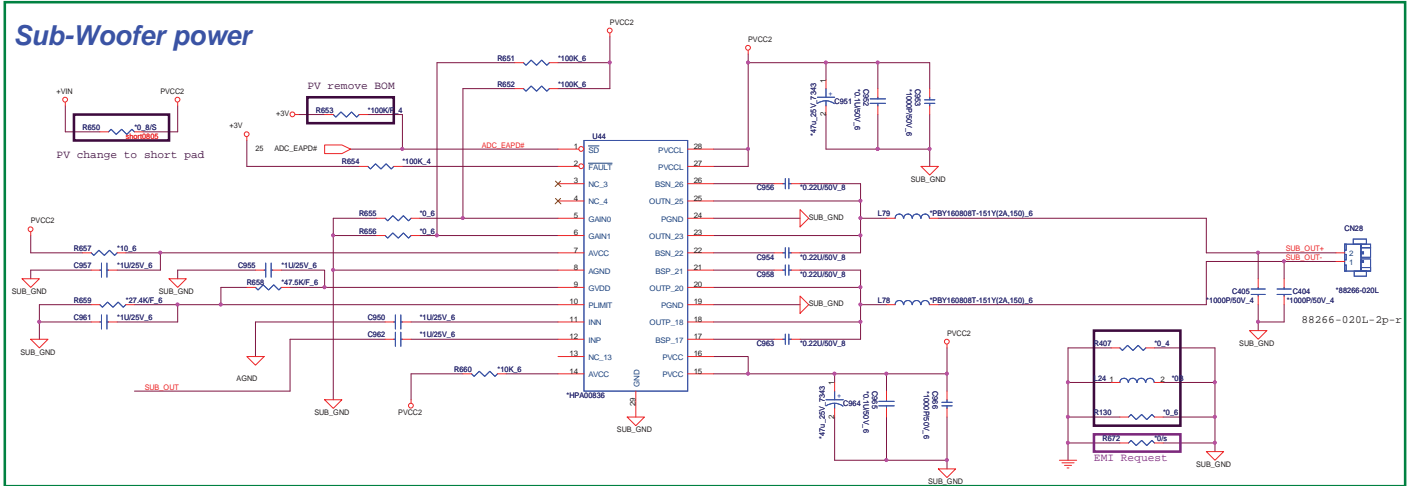
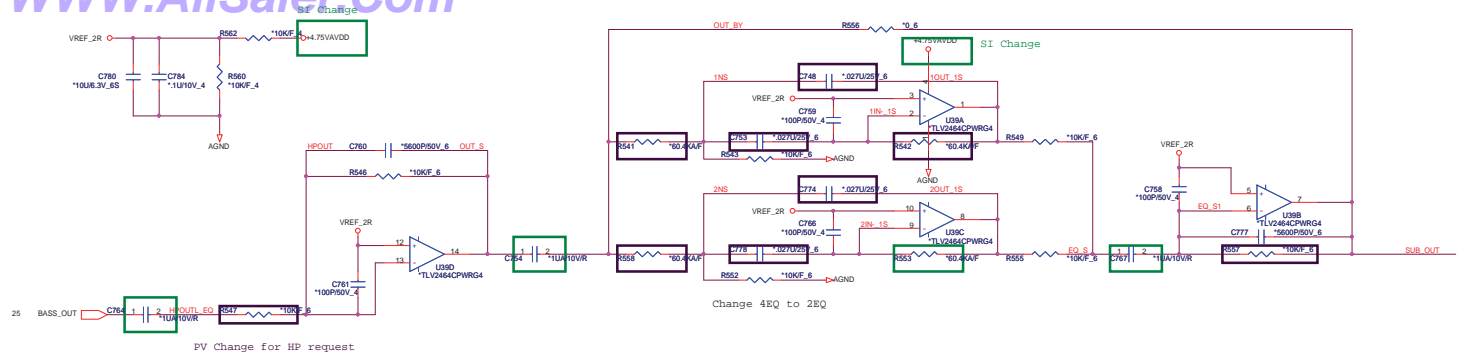




Accelerometer Sensor

SOT-LI8302DLTR interrupt pin default is low / active Hi, B108 need to programming 22h to change status from active Hi to low





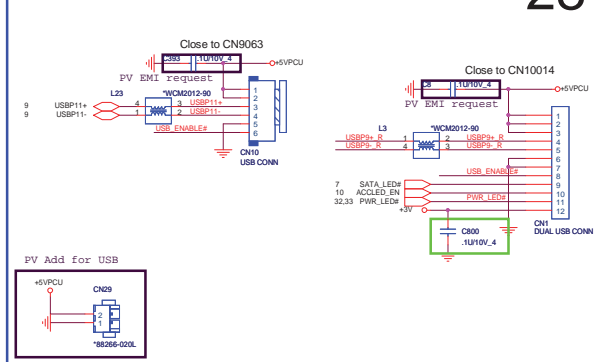
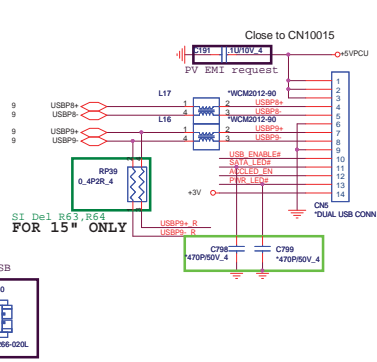
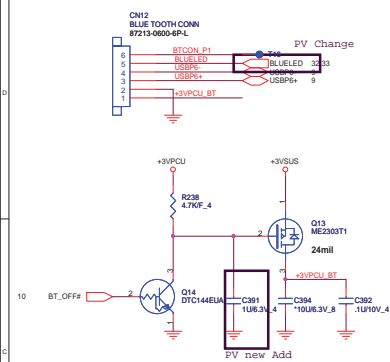
| GAIN1 | GAIN0 | dB |
|-------|-------|------|
| 0 | 0 | 12 |
| 0 | 1 | 18 |
| 1 | 0 | 23.6 |
| 1 | 1 | 36 |

PV Del R574, R241, R240

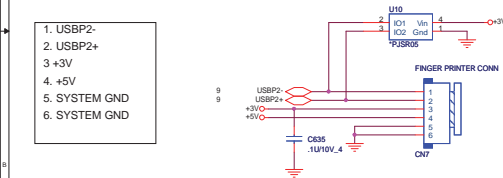
3V 2,3,7,8,9,10,11,12,13,21,22,23,24,25,26,28,29,30,31,32,33,34,37,40,41
4.75VAVDD 25
VIN 22,35,36,37,39,40,41,42,43

PROJECT : LX6_LX7
Quanta Computer Inc.

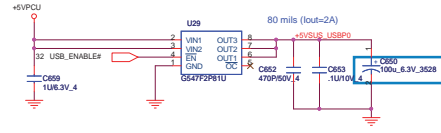
| | | |
|----------------------------------|----------------------|-----|
| Rev | Document Number | Rev |
| 1A | SUBWOOFER (EQ & AMP) | 1A |
| Date: Tuesday, February 03, 2010 | Sheet 27 of 44 | |



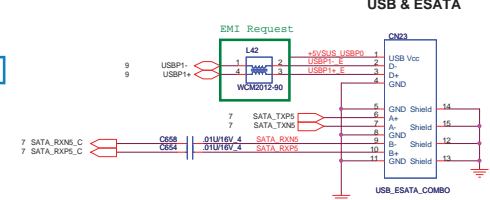
USB fingerprint CON



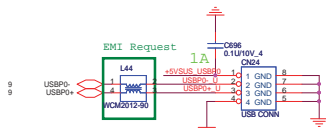
E-SATA



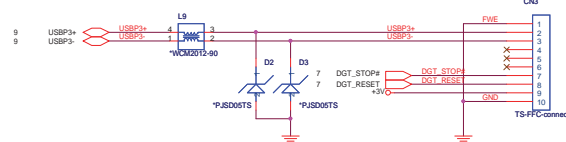
USB & ESATA



LEFT USB PORT



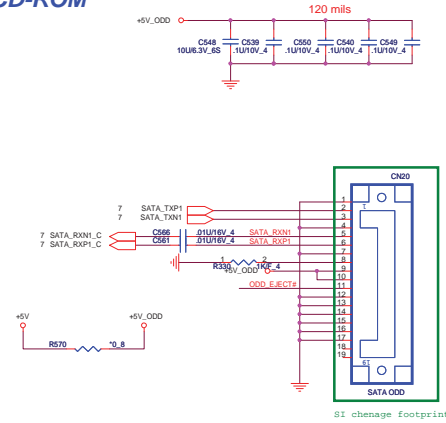
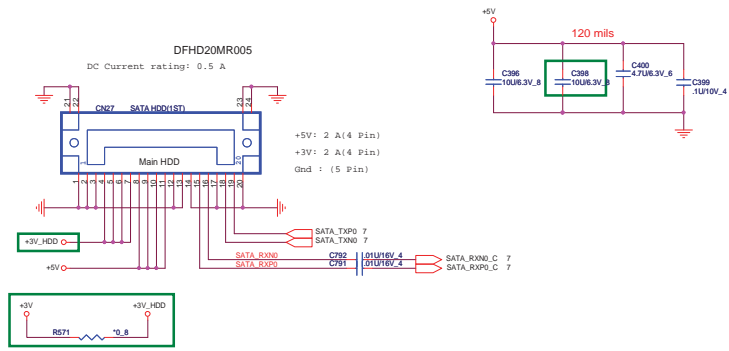
Touch screen for 15"



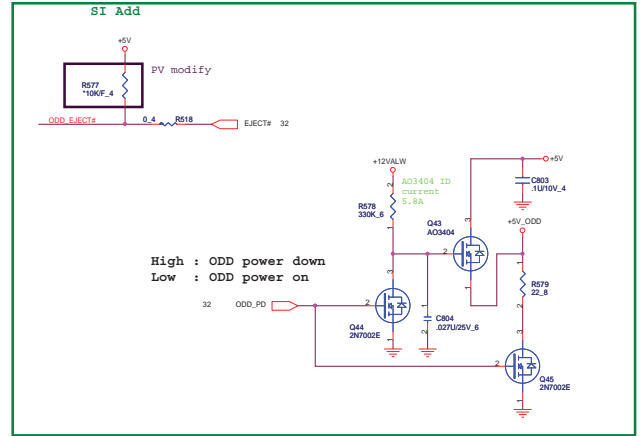
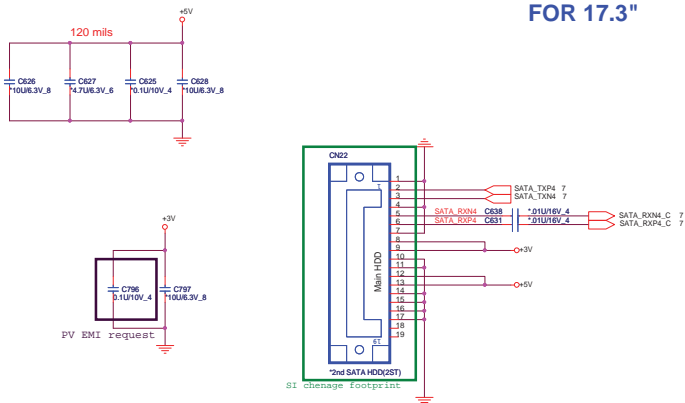
PROJECT : LX6_LX7
Quanta Computer Inc.

| | | |
|----------------------------------|--|----------------|
| Size Custom | Document Number BT/WC/FT/Touchscreen | Rev 1A |
| Date: Tuesday, February 02, 2010 | | Sheet 28 of 44 |

WWW.AliSaler.Com

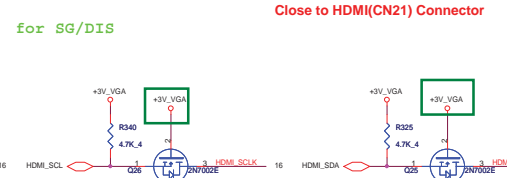
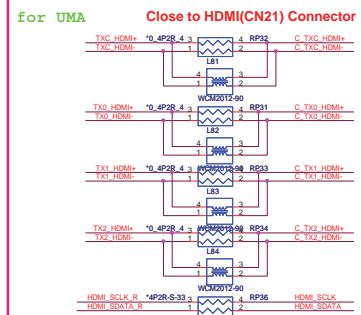
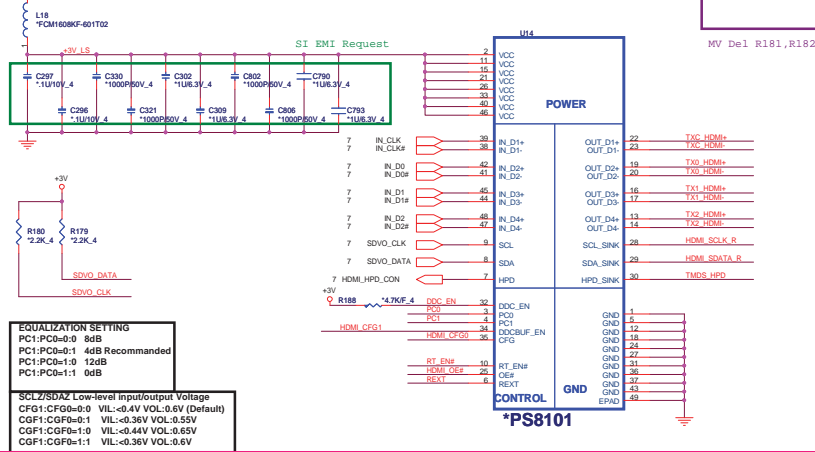


SATA_2 CONNECTOR FOR 17.3"

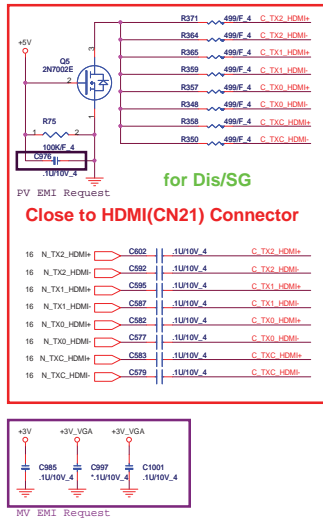


| | |
|---------|---|
| +3V | 2,3,7,8,9,10,11,12,13,21,22,23,24,25,26,27,28,29,31,32,33,34,37,40,41 |
| +5V | 11,21,22,23,25,26,28,31,33,34,41 |
| +12VALW | 22,34,39,41 |

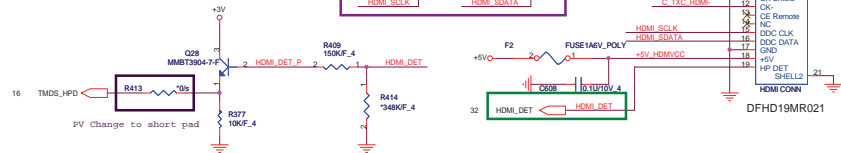
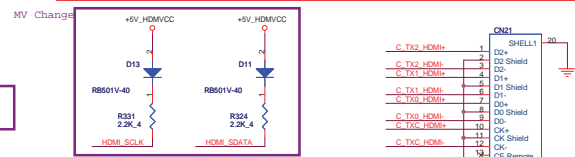
| | | | |
|----------------------------------|---------------|----------------------|----------|
| | | PROJECT : LX6_LX7 | |
| | | Quanta Computer Inc. | |
| NBS | Doc Number | Document Number | Rev |
| | ODD/HDD/AVANT | ODD/HDD/AVANT | 1A |
| Date: Tuesday, February 03, 2010 | | Sheet | 30 of 44 |



| Signals | | PDT | CHR | PIM |
|-----------|----|------|------|------|
| PCI | Ra | NC | 4.7K | NC |
| HDMI_CFG0 | Rb | NC | NC | NC |
| HDMI_CFG1 | Rc | 4.7K | NC | NC |
| AREXT | Rd | 499 | 1.2K | 4.7K |
| PCI | Re | NC | NC | 4.7K |
| HDMI_OE8 | Rf | NC | 4.7K | NC |
| PC0 | Rg | 4.7K | 4.7K | 4.7K |

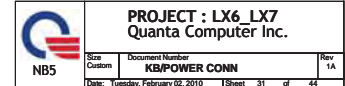
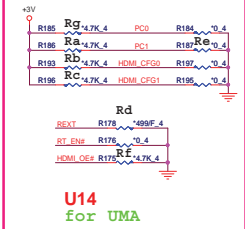


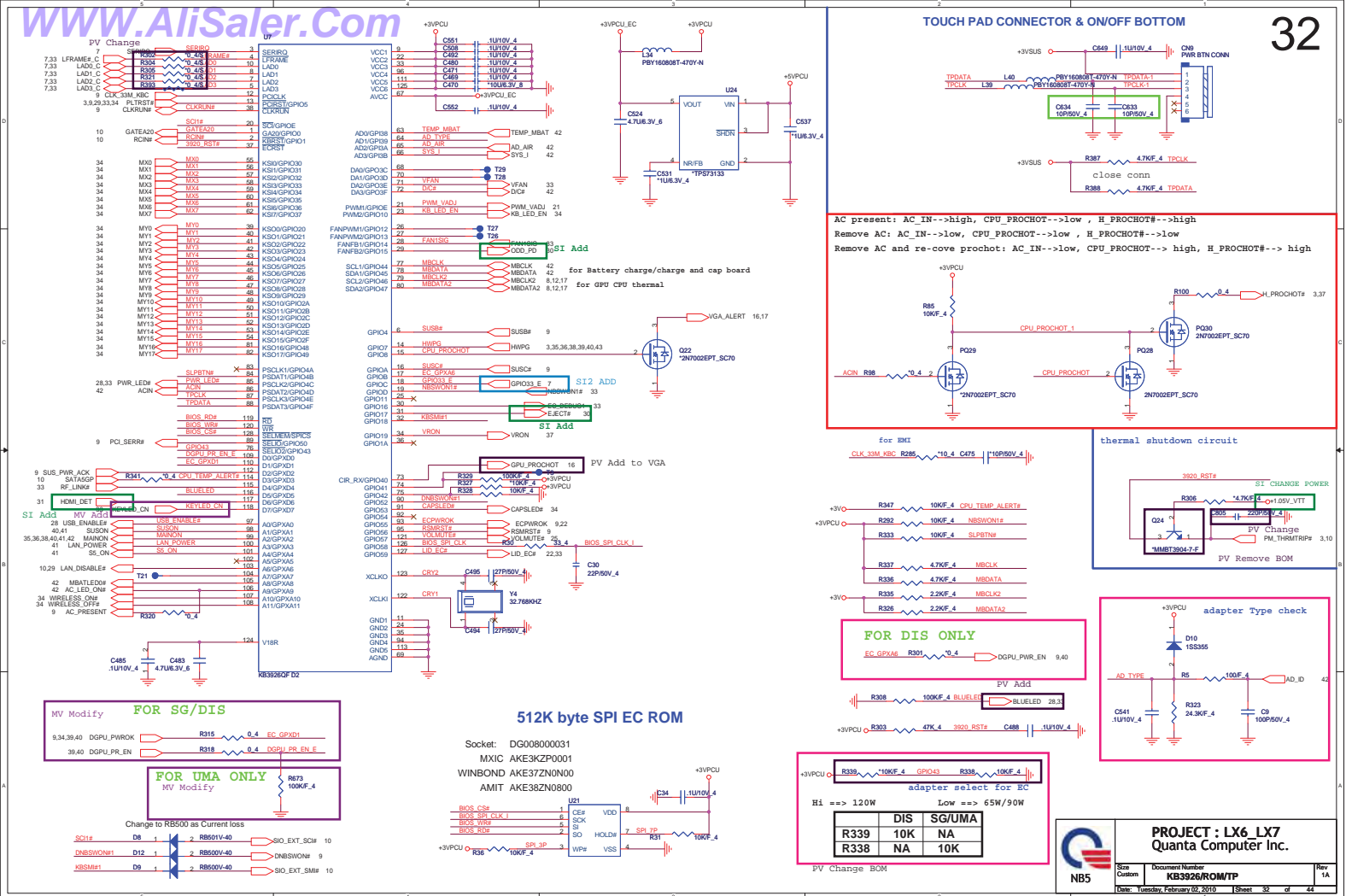
SI Del Q29,R378,R413,R415

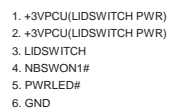
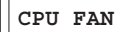


PV Del R374,R368,R378 BOM

for UMA/DIS/SG





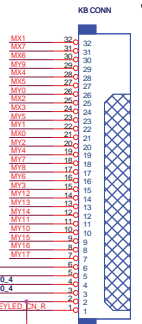


| | | |
|---|--------|---|
| | +3V | 2,3,7,8,9,10,11,12,13,21,22,23,24,25,26,27,28,29,30,31,32,34,37,40,41 |
|  | +5V | 11,21,22,23,25,26,28,30,31,34,41 |
| | +1.5V | 41 |
|  | +3VPCU | 7,22,28,32,34,35,36,39,41,42,43 |



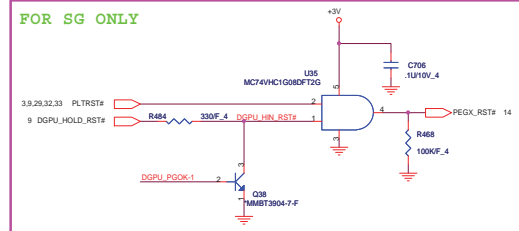
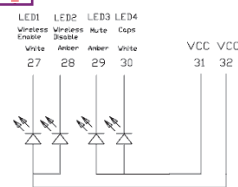
PROJECT : LX6_LX7
Quanta Computer Inc.

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| Size Custom | Document Number MINI PCIE CONN/FAN | Rev 1A |
| Date: Tuesday, February 02, 2010 | Sheet 33 of 44 | |



The schematic diagram illustrates the MV Adder circuit. A 32-bit bus labeled 'KEYED_CN' is connected to the input of a multiplexer 'MUX_4'. The output of 'MUX_4' is connected to a 10k resistor 'R074' and a capacitor 'C247' (10u10V_4). The circuit also includes a 3V power supply, a 200.4 resistor, and a 10k resistor 'R075'. The output is labeled 'MV Add'.

| | | | | | | | | | | | | | | | | | | |
|----|----|-----|-----|-----|----|-----|-----|----|----|----|----|-----|-----|----|-----|----|-----|-----|
| 12 | | 30 | 112 | | 50 | 119 | 56 | | 51 | | 13 | 123 | | 62 | 126 | 76 | 91 | 94 |
| 1 | | 110 | 115 | 127 | 35 | 118 | 125 | | 36 | | 41 | 122 | 107 | | 15 | 75 | 92 | 95 |
| 6 | 58 | 16 | 114 | | 21 | 117 | 28 | | 22 | | 27 | 121 | | | 90 | 60 | 93 | 96 |
| 9 | | 1 | 113 | 70 | 6 | 116 | 45 | 59 | 7 | | 12 | 120 | 129 | | 92 | 85 | 101 | 97 |
| 5 | 64 | 31 | 33 | 71 | 34 | 32 | 38 | | 37 | | 40 | 39 | | | 43 | 86 | 106 | 104 |
| 6 | | 46 | 48 | 72 | 49 | 47 | 53 | | 52 | 44 | 55 | 54 | | | 61 | 81 | 100 | 103 |
| 3 | | 2 | 4 | 73 | 5 | 3 | 9 | | 8 | | 11 | 10 | | 60 | 84 | 89 | 99 | 102 |
| 2 | | 17 | 19 | 74 | 20 | 18 | 24 | | 23 | 57 | 26 | 25 | | | 83 | 79 | 105 | 108 |
| | 7 | 11 | 13 | 18 | 14 | 10 | 17 | 15 | 16 | 4 | 23 | 22 | 19 | 20 | 21 | 24 | 25 | 26 |

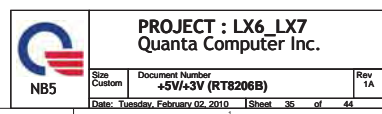


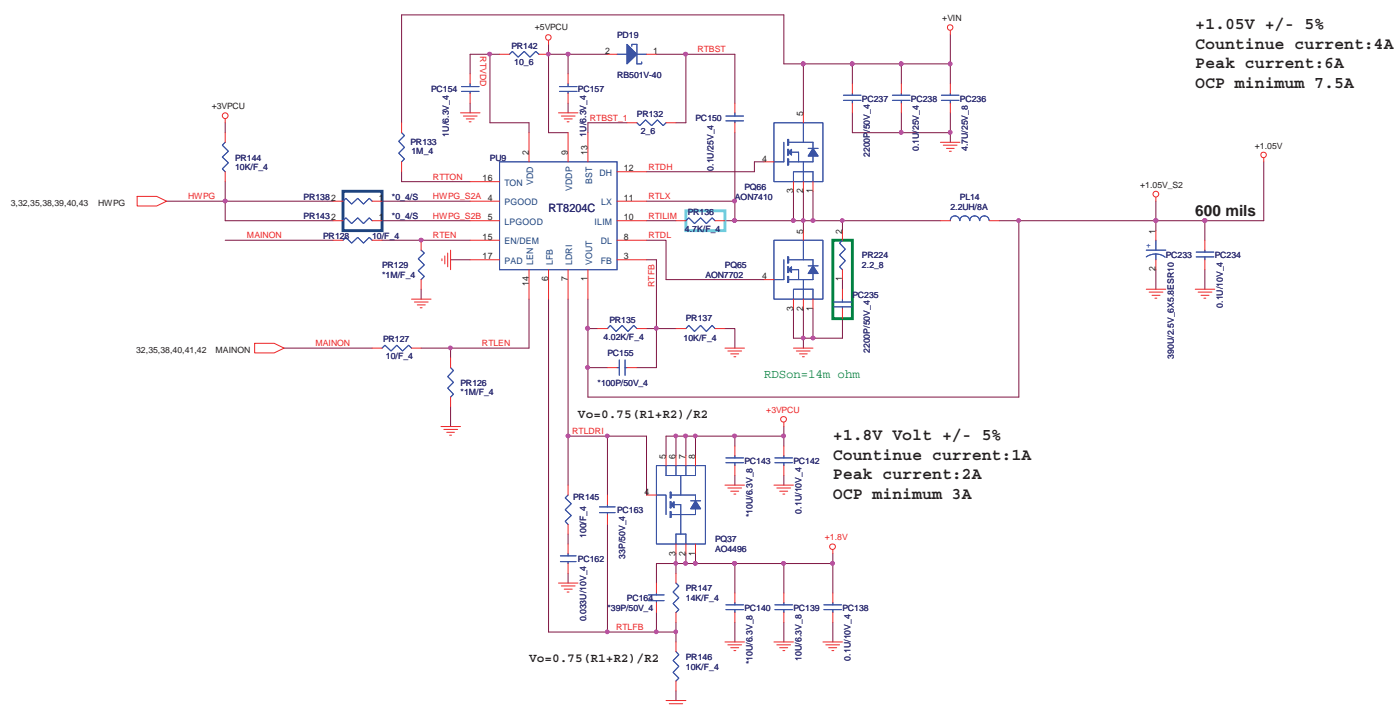
| | |
|-----------|---|
| +3V | 2,3,7,8,9,10,11,12,13,21,22,23,24,25,26,27,28,29,30,31,32,33,37,40,41 |
| +5V | 11,21,32,33,35,26,28,30,31,33,41 |
| +3VPCU | 7,22,28,32,33,35,36,39,41,42,43 |
| +3V_VGA | 15,16,17,18,31,39 |
| +12VALV | 22,30,39,41 |
| +1.0V_VGA | 14,16,18,40 |
| +1.5V_VGA | 15,18,19,20,39 |
| +1.8V_VGA | 14,16,18,39 |



PROJECT : LX6_LX7
Quanta Computer Inc.

| | | |
|---------------------------------|------------------------------|----------------|
| Size Custom | Document Number KB | Rev 1A |
| Date Tuesday, February 02, 2010 | | Sheet 34 of 44 |





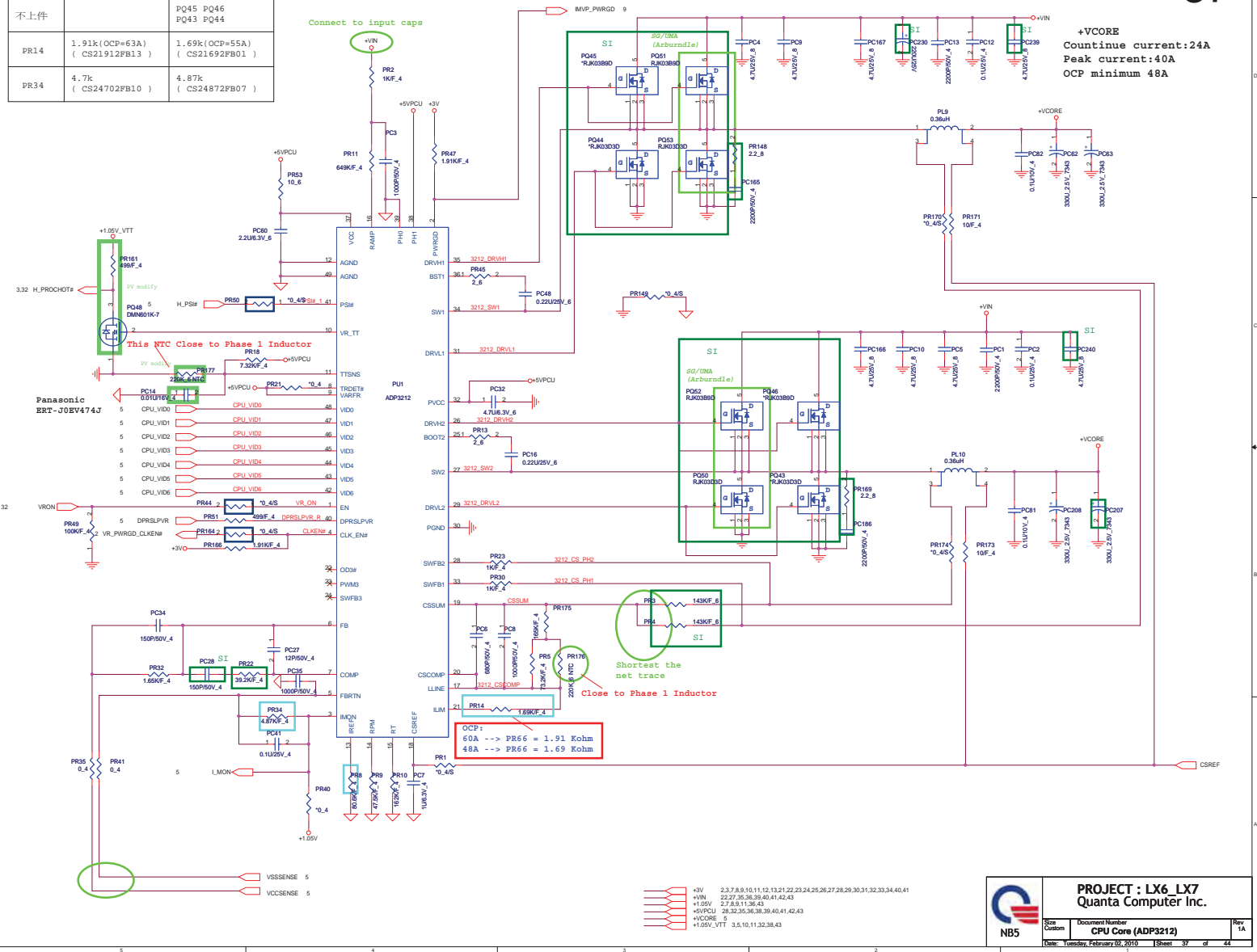
| | | |
|--|--------|---------------------------------|
| | +1.8V | 5,10,11,21,41 |
| | +VIN | 22,27,35,37,39,40,41,42,43 |
| | +1.05V | 2,7,8,9,11,37,43 |
| | +3VPCU | 7,22,28,32,33,34,35,39,41,42,43 |
| | +5VPCU | 28,32,35,37,38,39,40,41,42,43 |



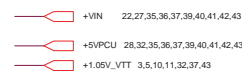
PROJECT : LX6_LX7
Quanta Computer Inc.

| | | |
|----------------------------------|--|-----------|
| Size Custom | Document Number +1.05V/+1.8V (RT8204C) | Rev 1A |
| Date: Tuesday, February 02, 2010 | Sheet 36 of 44 | |

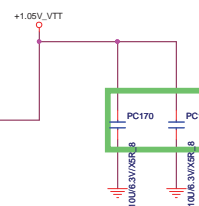
| | DES (Clarksfield) | SG/UMA (Arbunrdle) |
|------|-----------------------------------|-----------------------------------|
| 不上件 | | PQ45 PQ46 PQ43 PQ44 |
| PR14 | 1.91k(OCp=63A) (CS21912PB13) | 1.69k(OCp=55A) (CS21692PB01) |
| PR34 | 4.7k (CS24702PB10) | 4.87k (CS24872PB07) |




| | Clarksfield | Arraindle |
|-------|------------------------|------------------------|
| PU10 | VT358 (AL000358U00) | VT357 (AL000357U00) |
| PR160 | 40.2K (CS34022FB15) | 36K (CS33602FB15) |
| PR158 | 5.76K (CS25762FB01) | 6.49K (CS26492FB23) |



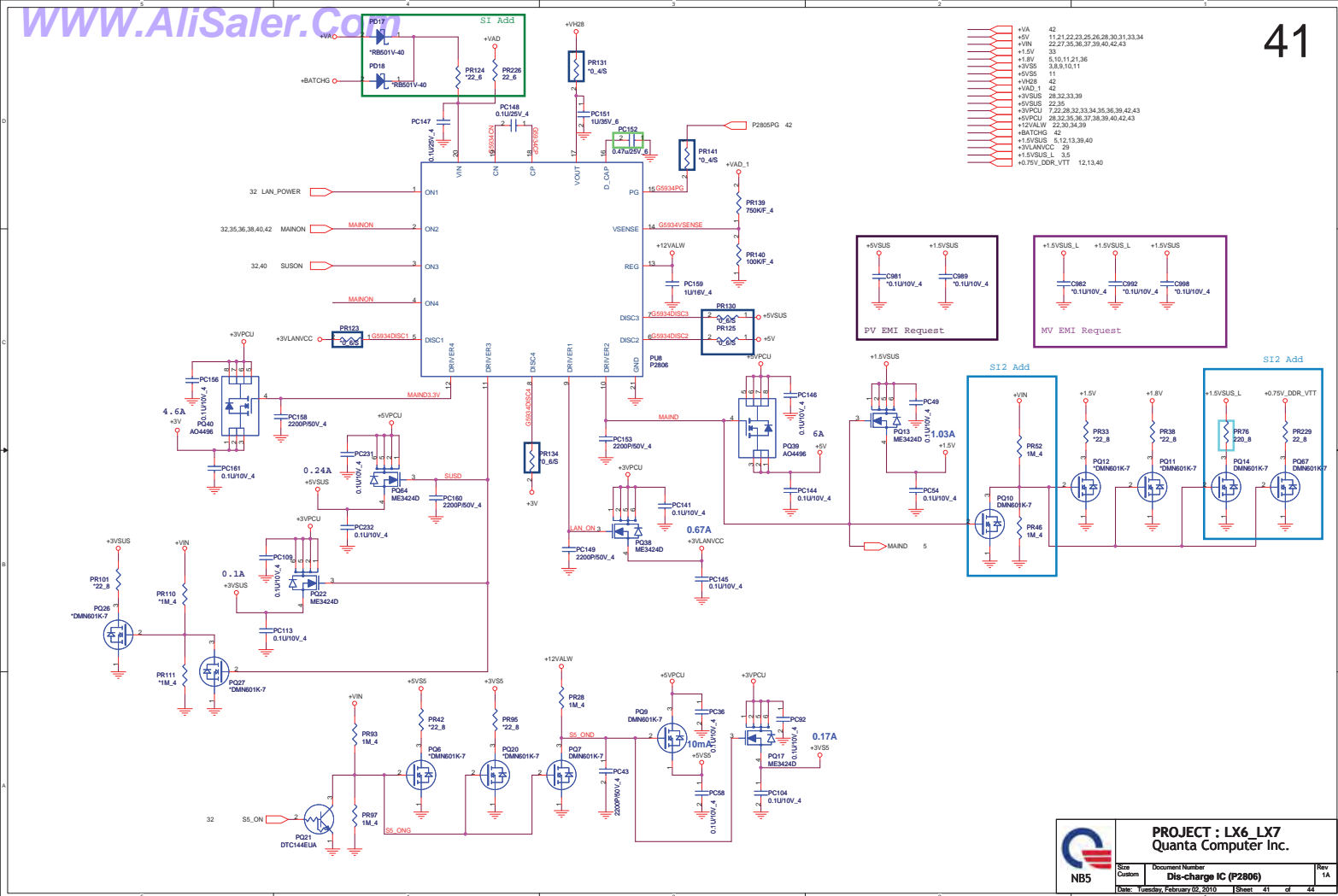
```
+1.05V_VTT +/- 5%
Continue current:12A
Peak current:15A
OCP minimum 18A
```

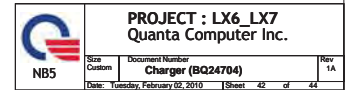


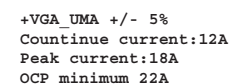
| | | | |
|---|---|--|------------------|
|  NB5 | PROJECT : LX6_LX7 Quanta Computer Inc. | | |
| | Size Custom | Document Number +1.05V_VTT (VT358) | Rev 1A |
| Date: Tuesday, February 02, 2010 | | Sheet 36 of 44 | |




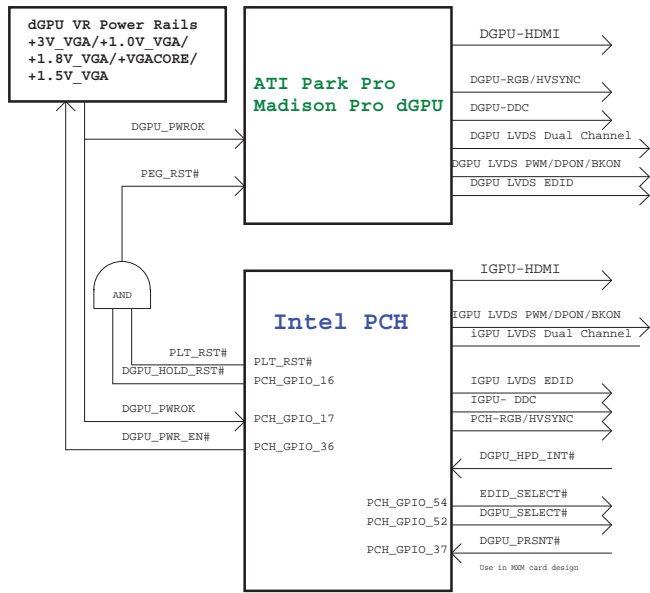




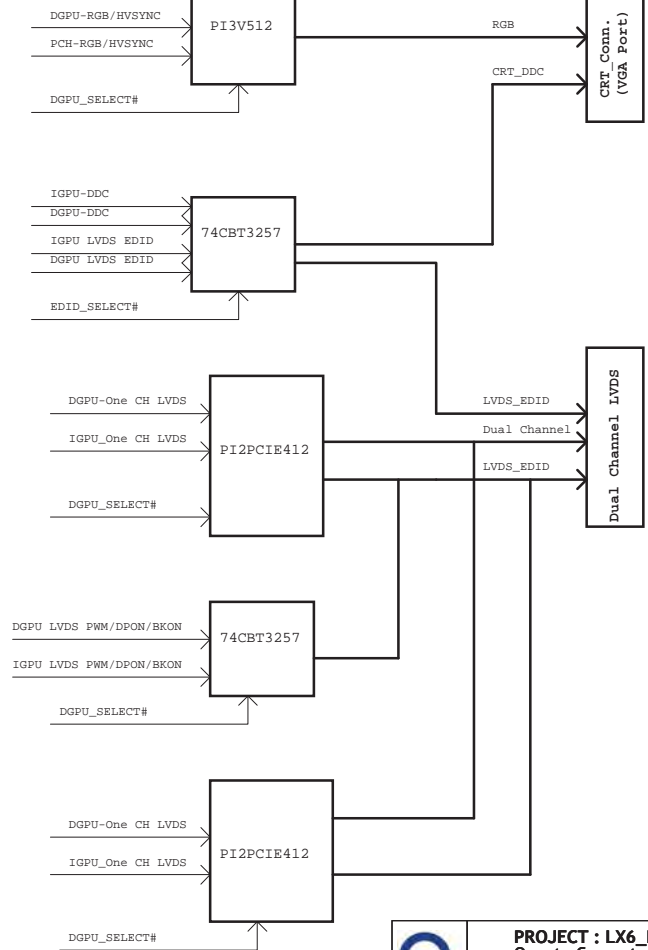




| | | | |
|--|---|--|-----------|
|  NB5 | PROJECT : LX6_LX7 Quanta Computer Inc. | | |
| | Size Custom | Document Number UMA GPU CORE (RT8152C) | Rev 1A |
| Date: Tuesday, February 02, 2010 Sheet 43 of 44 | | | |



| Switchable GPIOs | Descriptions |
|------------------|----------------|
| PCH_GPIO52 | DGPU_SELECT# |
| PCH_GPIO16 | DGPU_HOLD_RST# |
| PCH_GPIO36 | DGPU_PWR_EN# |
| PCH_GPIO17 | DGPU_PWR_OK |
| PCH_GPIO54 | EDID_ELECT# |
| PCH_GPIO37 | DGPU_PRSENT# |
| | |



| | | |
|------------------|---|--|
| | PROJECT : LX6_LX7 Quanta Computer Inc. | |
| | Size Custom | Document Number Switch Blockdiagram |
| Rev 1A | Date: Tuesday, February 02, 2010 | Sheet 44 of 44 |